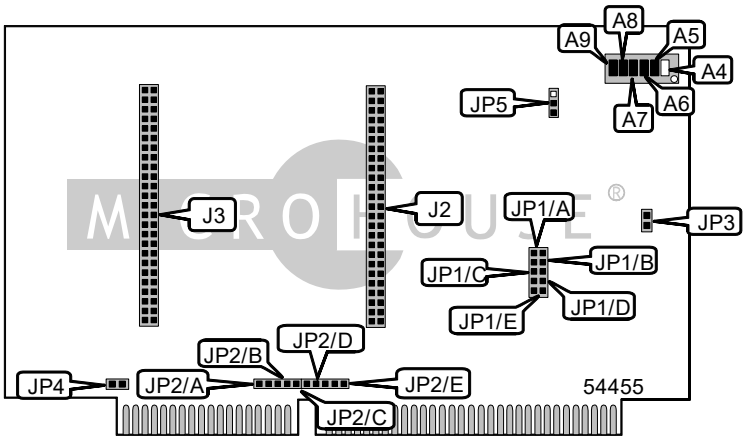
IDUSTRIAL COMPUTER SOURCE
D I O / 4 8 S / A T - P

Card Type	Digital I/O card
Chipset	Unidentified
I/O Options	50-pin connectors (2)
Data Bus	16-bit ISA



CONNECTIONS			
Function	Location	Function	Location
50-pin termination panel connector	J2	50-pin termination panel connector	J3

USER CONFIGURABLE SETTINGS		
Function	Label	Position
I/O buffers always enabled	JP5/Pins 2 & 3	Closed
I/O buffers disabled	JP5/Pins 2 & 3	Open
I/O buffers controlled by control register	JP5/Pins 1 & 2	Closed
I/O buffers not controlled by control register	JP5/Pins 1 & 2	Open
Note: JP5 may be screened on the board as TST/BEN. TST is equal to Pins 1 & 2 and BEN is equal to Pins 2 & 3.		

BASE I/O ADDRESS SELECTION						
Setting	A4	A5	A6	A7	A8	A9
000h	On	On	On	On	On	On
010h	Off	On	On	On	On	On
020h	On	Off	On	On	On	On
030h	Off	Off	On	On	On	On
040h	On	On	Off	On	On	On
3B0h	Off	Off	On	Off	Off	Off
3C0h	On	On	Off	Off	Off	Off
3D0h	Off	On	Off	Off	Off	Off
3E0h	On	Off	Off	Off	Off	Off
3F0h	Off	Off	Off	Off	Off	Off
Note:	A total of 64 base address settings are available. The switches are a binary representation of the decimal memory addresses. A9 is the Most Significant Bit and switch A4 is the Least Significant Bit. The switches have the following decimal values: A4=16, A5=32, A6=64, A7=128, A8=256, A9=512. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)					

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IRQ SETTINGS					
IRQ	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E
3	Closed	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open
5	Open	Open	Closed	Open	Open
6	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed
10	Closed	Closed	Closed	Closed	Open
11	Closed	Closed	Closed	Open	Closed
12	Closed	Closed	Open	Closed	Closed
14	Open	Closed	Closed	Closed	Closed
15	Closed	Open	Closed	Closed	Closed

IRQ SETTINGS (CON'T)					
IRQ	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E
3	Open	Closed	Closed	Closed	Closed
4	Closed	Open	Closed	Closed	Closed
5	Closed	Closed	Open	Closed	Closed
6	Closed	Closed	Closed	Open	Closed
7	Closed	Closed	Closed	Closed	Open
10	Open	Open	Open	Open	Closed
11	Open	Open	Open	Closed	Open
12	Open	Open	Closed	Open	Open
14	Closed	Open	Open	Open	Open
15	Open	Closed	Open	Open	Open

DIAGNOSTIC LED(S)			
LED	Color	Status	Condition
CR1	Unidentified	On	Every time an interrupt is generated
CR1	Unidentified	Off	No interrupt is generated

MISCELLANEOUS TECHNICAL NOTES
You can install a total of 48 serial, parallel, IDE, game, or floppy ports.