ADVANTECH CO., LTD
PCL-730
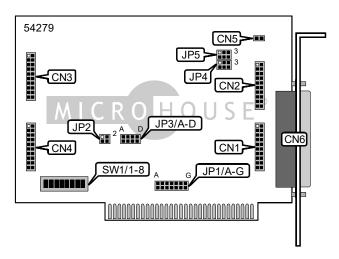
Card Type	Digital I/O card
Chip Set	Unidentified
Maximum Onboard Memory	Unidentified
I/O Options	Input channel connectors (2), output channel connectors (2), 37-pin input/output connector
Hard Drives supported	None
Floppy drives supported	None
Data Bus	8-bit ISA
Card Size	Half-length, half-height card

CONNECTIONS			
Function	Label	Function	Label
20-pin isolated output connector	CN1	20-pin digital input connector	CN4
20-pin isolated input connector	CN2	Isolated output ground terminal	CN5
20-pin digital output connector	CN3	37-pin input/output connector	CN6

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Interrupt triggered on rising edge	JP2	Pins 1 & 3 closed
Interrupt triggered on falling edge	JP2	Pins 2 & 4 closed

VERSION SELECTION		
Setting	JP4	JP5
A1	Pins 1 & 2, 4 & 5 closed	Pins 1 & 2, 4 & 5 closed
B2	Pins 2 & 3, 5 & 6 closed	Pins 2 & 3, 5 & 6 closed
Note: These jumpers control the pin assignments for CN1, CN2, and CN6.		

INERRUPT SOURCE SELECTION				
Channel	JP3/A	JP3/B	JP3/C	JP3/D
Isolated DI channel 0	Closed	Open	Open	Open
Isolated DI channel 1	Open	Closed	Open	Open
TTL DI channel 0	Open	Open	Closed	Open
TTL DI channel 1	Open	Open	Open	Closed

ADVANTECH CO., LTD

PCL-730

BASE I/O ADDRESS SELECTION								
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
200h	Off	On	On	On	On	On	On	On
204h	Off	On	On	On	On	On	On	Off
208h	Off	On	On	On	On	On	Off	On
20Ch	Off	On	On	On	On	On	Off	Off
210h	Off	On	On	On	On	Off	On	On
í 300h	Off	Off	On	On	On	On	On	On
3E0h	Off	Off	Off	Off	Off	On	On	On
3E4h	Off	Off	Off	Off	Off	On	On	Off
3E8h	Off	Off	Off	Off	Off	On	Off	On
3ECh	Off	Off	Off	Off	Off	On	Off	Off
3FOh	Off	Off	Off	Off	Off	Off	On	On

Note:	A total of 125 valid base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/8 is the Least Significant Bit and switch SW1/1 is the Most Significant Bit. The switches have the following decimal values: SW1/1=512, SW1/2=256, SW1/3=128, SW1/4=64, SW1/5=32, SW1/6=16, SW1/7=8, SW1/8=4. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)

IRQ INTERRUPT SELECTION							
IRQ	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F	JP1/G
IRQ2	Closed	Open	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open	Open
IRQ7	Open	Open	Open	Open	Open	Closed	Open
í Disabled	Open	Open	Open	Open	Open	Open	Closed