

GENOA SYSTEMS CORPORATION

GALAXY 3100QIC-02 TAPE CONTROLLER

Device Type

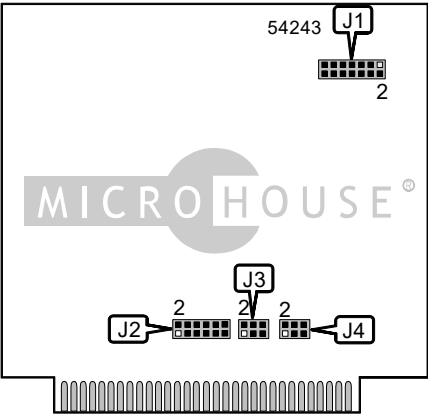
Chip Set

Data Bus

Tape drive controller

Unidentified

8-bit ISA



DMA CHANNEL SELECTION		
Channel	J3	J4
1	Pins 1 & 2 closed	Pins 1 & 2 closed
2	Pins 3 & 4 closed	Pins 3 & 4 closed
3	Pins 5 & 6 closed	Pins 5 & 6 closed

BASE I/O ADDRESS SELECTION							
Address	J1/pins 1 & 2	J1/pins 3 & 4	J1/pins 5 & 6	J1/pins 7 & 8	J1/pins 9 & 10	J1/pins 11 & 12	J1/pins 13 & 14
288H	Open	Closed	Closed	Closed	Open	Closed	Open
300H	Closed	Closed	Closed	Closed	Closed	Open	Open
338H	Open	Open	Open	Closed	Closed	Open	Open

INTERRUPT SELECTION	
IRQ	J2
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
6	Pins 9 & 10 closed
7	Pins 11 & 12 closed