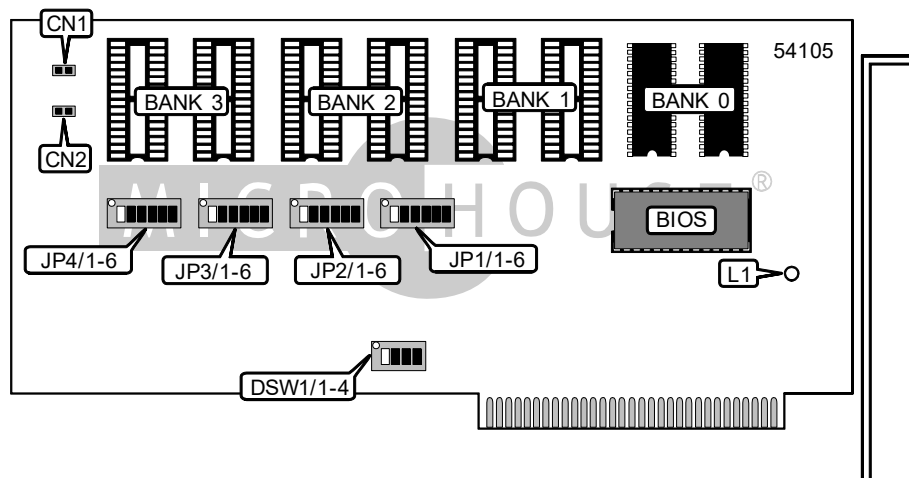


ACQUTEK CORPORATION

PM-7008

Card Type	Virtual memory card
Chip Set	Unidentified
Maximum Onboard Memory	256KB RAM or 512KB ROM
I/O Options	Watch dog timer connectors (2)
Hard Drives supported	None
Floppy drives supported	None
Data Bus	8-bit ISA



CONNECTIONS			
Function	Label	Function	Label
Watch dog timer reset connector (out)	CN1	Watch dog timer reset connector (in)	CN2

DIAGNOSTIC LED	
Function	Label
Watch dog timer status indicator	L1
Note: Exact indicator status is unidentified.	

MEMORY CONFIGURATION
The memory sockets (bank 0-3) on the PM-7008 accepts SRAM chips (62256) or EPROM chips(27512). The sockets may be filled in any order and a combination of SRAM and EPROM may be used. If ROM is used, the user must program the EPROM chips prior to installation.

RAM/ROM SWITCH SETTINGS			
Setting	Label	1-3	4-6
RAM enabled (bank 0)	JP1	On	Off
ROM enabled (bank 0)	JP1	Off	On
RAM enabled (bank 1)	JP2	On	Off
ROM enabled (bank 1)	JP2	Off	On
RAM enabled (bank 2)	JP3	On	Off
ROM enabled (bank 2)	JP3	Off	On
RAM enabled (bank 3)	JP4	On	Off
ROM enabled (bank 3)	JP4	Off	On

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BIOS ADDRESS SELECTION		
Address	DSW1/3	DSW1/4
D0000h	Off	Off
D4000h	On	Off
D8000h	Off	On
DC000h	On	On

BASE I/O ADDRESS SELECTION		
Setting	DSW1/1	DSW1/2
200h	Off	Off
280h	On	Off
300h	Off	On
Reserved	On	On