MULTI-TECH SYSTEMS, INC.
ISI2834/4, ISI2834/8, ISI2834/EC

Card Type	Modem (asynchronous)
Processor Type	80186
Processor Speed	16MHz
Chip Set	Unidentified
Maximum Onboard Memory	256KB RAM
Maximum Data Rate	28.8Kbps x 4 (8 with daughtercard)
Maximum Fax Rate	14.4Kbps x 4 (8 with daughtercard)
Data Bus	16-bit ISA
Fax Class	Class II
Data Modulation Protocol	Bell 103A/212A
	ITU-T V.22, V.22bis, V.32, V.32bis, V.34
	AT&T V.32terbo
Fax Modulation Protocol	ITU-T V.17, V.21CH2, V.27ter, V.29
Error Correction/Compression	MNP5, V.42, V.42bis
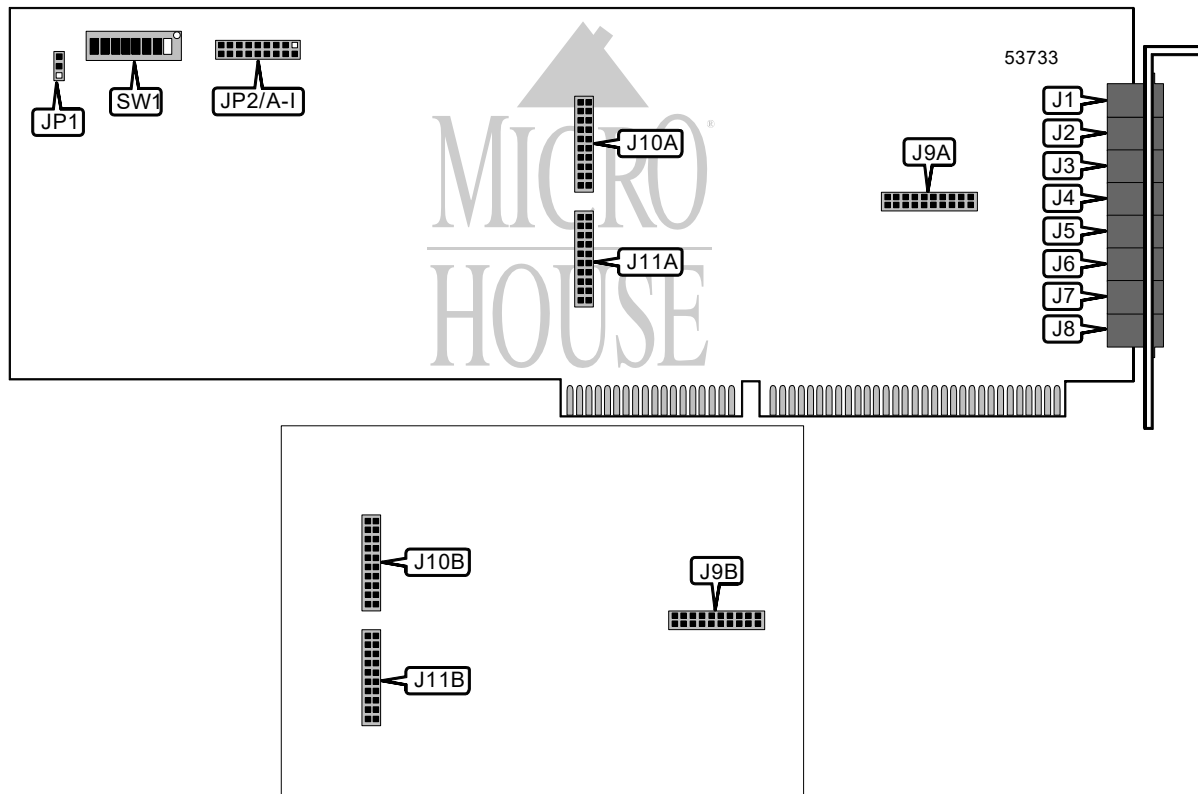
CONNECTIONS			
Function	**Label**	**Function**	**Label**
Telephone line 1 out	J1	Telephone line 8 out	J8
Telephone line 2 out	J2	Header to daughterboard J9B	J9A
Telephone line 3 out	J3	Header to main board J9A	J9B
Telephone line 4 out	J4	Header to daughterboard J10B	J10A
Telephone line 5 out	J5	Header to main board J10A	J10B
Telephone line 6 out	J6	Header to daughterboard J11B	J11A
Telephone line 7 out	J7	Header to main board J11A	J11B
Note: J5 through J8 will not be active unless the daughtercard is installed.			

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USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Daughtercard is not installed	JP1	Pins 1 & 2 closed
Daughtercard is installed	JP1	Pins 2 & 3 closed

INTERRUPT SELECTION									
Setting	**JP2/A**	**JP2/B**	**JP2/C**	**JP2/D**	**JP2/E**	**JP2/F**	**JP2/G**	**JP2/H**	**JP2/I**
2	Closed	Open	Open	Open	Open	Open	Open	Open	Open
í 3	Open	Closed	Open	Open	Open	Open	Open	Open	Open
4	Open	Open	Closed	Open	Open	Open	Open	Open	Open
5	Open	Open	Open	Closed	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Closed	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Open	Open	Open	Closed

BASE I/O ADDRESS SELECTION								
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**	**SW1/7**	**SW1/8**
000h	On	On	On	On	On	On	On	On
008h	Off	On	On	On	On	On	On	On
010h	On	Off	On	On	On	On	On	On
018h	Off	Off	On	On	On	On	On	On
020h	On	On	Off	On	On	On	On	On
í 200h	On	On	On	On	On	On	Off	On
7D8h	Off	Off	On	Off	Off	Off	Off	Off
7E0h	On	On	Off	Off	Off	Off	Off	Off
7E8h	Off	On	Off	Off	Off	Off	Off	Off
7F0h	On	Off	Off	Off	Off	Off	Off	Off
7F8h	Off	Off	Off	Off	Off	Off	Off	Off

Note: A total of 256 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/8 is the Most Significant Bit and switch SW1/1 is the Least Significant Bit. The switches have the following decimal values: SW1/8=1024, SW1/7=512, SW1/6=256, SW1/5=128, SW1/4=64, SW1/3=32, SW1/2=16, SW1/1=8. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

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SUPPORTED STANDARD COMMANDS
Basic AT Commands
+++, 'comma', A/
A, B, D, E, H, O, P, T, V, W, X, Y, Z
&D, &F, &G, &P, &S, &T, &W
S-Registers
S0, S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S30
Note: See MHI documentation for complete information.

Proprietary AT Command Set

See MULTI-TECH SYSTEMS, INC. ISI3334/8 for a full command summary.