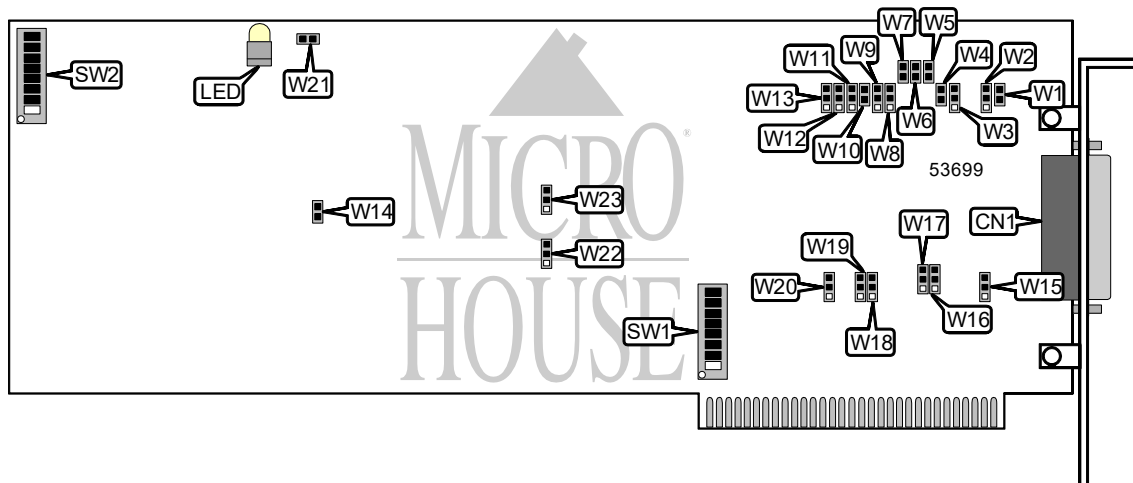# PHILIPS CONSUMER ELECTRONICS, CO.
LWSI-CONTROLLER (PART NUMBERS 12 NC 5122 291 9952X, 12 NC 5122 292 1791X, AND 12 NC 5122 292 0780X)

Card Type	Serial terminal emulator
Processor	8088
Processor Speed	Unidentified
Chipset	Zilog Z8530
Maximum Onboard Memory	24KB SRAM
I/O Options	Serial port
Data Bus	8-bit ISA
Card Size	Full height, full length

CONNECTIONS	
Function	**Label**
X.24 or V.24 serial port	CN1
Note: Using the jumpers on the board specialized configurations for the serial port may be used.	

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í 24KB of SRAM installed on card	SW1/4	On
16KB of SRAM installed on card	SW1/4	Off
í Factory configured - do not alter	SW2/1	Off
í Factory configured - do not alter	SW2/2	Off
í Factory configured - do not alter	SW2/8	On
Pin 14 of serial port connected to X.24 TB signal	W1	Closed
Pin 14 of serial port has no function	W1	Open
Pin 16 of serial port connected to X.24 RB signal	W4	Closed
Pin 16 of serial port has no function	W4	Open
Normal delay on V.24 signal 107	W5	Closed
Extra delay on V.24 signal 107	W5	Open
Normal delay on V.24 signal 104	W6	Closed
Extra delay on V.24 signal 104	W6	Open

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USER CONFIGURABLE SETTINGS (CON'T)		
Setting	**Label**	**Position**
Normal delay on V.24 signal 114	W7	Closed
Extra delay on V.24 signal 114	W7	Open
Pin 19 of serial port connected to X.24 BB signal	W10	Closed
Pin 19 of serial port has no function	W10	Open
Signal Element used as timer	W11	Pins 2 & 3 closed
Internal clock source used as timer	W11	Pins 1 & 2 closed
Internal clock source used for receive clock	W12	Pins 1 & 2 closed
External clock source used for receive clock	W12	Pins 2 & 3 closed
Internal clock source used for transmit clock	W13	Pins 2 & 3 closed
External clock source used for transmit clock	W13	Pins 1 & 2 closed
í Processor clock enabled	W14	Closed
Processor clock disabled	W14	Open
í Peripheral clock enabled	W21	Closed
Peripheral clock disabled	W21	Open

BASE I/O ADDRESS SELECTION					
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**W22**	**W23**
80000h	Off	Off	Off	Pins 2 & 3 closed	Pins 2 & 3 closed
84000h	Off	Off	On	Pins 2 & 3 closed	Pins 2 & 3 closed
88000h	Off	On	Off	Pins 2 & 3 closed	Pins 2 & 3 closed
8C000h	Off	On	On	Pins 2 & 3 closed	Pins 2 & 3 closed
90000h	On	Off	Off	Pins 2 & 3 closed	Pins 2 & 3 closed
94000h	On	Off	On	Pins 2 & 3 closed	Pins 2 & 3 closed
98000h	On	On	Off	Pins 2 & 3 closed	Pins 2 & 3 closed
9C000h	On	On	On	Pins 2 & 3 closed	Pins 2 & 3 closed
A0000h	Off	Off	Off	Pins 2 & 3 closed	Pins 1 & 2 closed
A4000h	Off	Off	On	Pins 2 & 3 closed	Pins 1 & 2 closed
A8000h	Off	On	Off	Pins 2 & 3 closed	Pins 1 & 2 closed
AC000h	Off	On	On	Pins 2 & 3 closed	Pins 1 & 2 closed
B0000h	On	Off	Off	Pins 2 & 3 closed	Pins 1 & 2 closed
B4000h	On	Off	On	Pins 2 & 3 closed	Pins 1 & 2 closed
B8000h	On	On	Off	Pins 2 & 3 closed	Pins 1 & 2 closed
BC000h	On	On	On	Pins 2 & 3 closed	Pins 1 & 2 closed
C0000h	Off	Off	Off	Pins 1 & 2 closed	Pins 2 & 3 closed
C4000h	Off	Off	On	Pins 1 & 2 closed	Pins 2 & 3 closed
C8000h	Off	On	Off	Pins 1 & 2 closed	Pins 2 & 3 closed
CC000h	Off	On	On	Pins 1 & 2 closed	Pins 2 & 3 closed
D0000h	On	Off	Off	Pins 1 & 2 closed	Pins 2 & 3 closed
D4000h	On	Off	On	Pins 1 & 2 closed	Pins 2 & 3 closed
D8000h	On	On	Off	Pins 1 & 2 closed	Pins 2 & 3 closed
DC000h	On	On	On	Pins 1 & 2 closed	Pins 2 & 3 closed

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BASE I/O ADDRESS SELECTION (CON'T)					
Setting	SW1/1	SW1/2	SW1/3	W22	W23
E0000h	Off	Off	Off	Pins 1 & 2 closed	Pins 1 & 2 closed
E4000h	Off	Off	On	Pins 1 & 2 closed	Pins 1 & 2 closed
E8000h	Off	On	Off	Pins 1 & 2 closed	Pins 1 & 2 closed
EC000h	Off	On	On	Pins 1 & 2 closed	Pins 1 & 2 closed
F0000h	On	Off	Off	Pins 1 & 2 closed	Pins 1 & 2 closed
F4000h	On	Off	On	Pins 1 & 2 closed	Pins 1 & 2 closed
F8000h	On	On	Off	Pins 1 & 2 closed	Pins 1 & 2 closed
FC000h	On	On	On	Pins 1 & 2 closed	Pins 1 & 2 closed

INTERRUPT SELECTION				
Setting	SW1/5	SW1/6	SW1/7	SW1/8
2	On	On	On	Off
3	On	On	Off	On
5	On	Off	On	On
7	Off	On	On	On

LINK ADDRESS SELECTION					
Setting	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7
1	Off	Off	Off	Off	Off
3	Off	Off	Off	Off	On
5	Off	Off	Off	On	Off
7	Off	Off	Off	On	On
9	Off	Off	On	Off	Off
55	On	On	Off	On	On
57	On	On	On	Off	Off
59	On	On	On	Off	On
61	On	On	On	On	Off
63	On	On	On	On	On

Note: A total of 32 link address settings are available. The switches are a binary representation of the decimal link addresses. SW2/3 is the Most Significant Bit and switch SW2/7 is the Least Significant Bit. The switches have the following decimal values: SW2/7=2, SW2/6=4, SW2/5=8, SW2/4=16, SW2/3=32. Turn on the switches and add the values of the switches that are on to obtain the correct link address. (Off=0, On=1)

PIN 2 CONFIGURATION	
Setting	W3
X.24 TA signal	Pins 2 & 3 closed
V.24 103 signal	Pins 1 & 2 closed
No function	Open

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PIN 3 CONFIGURATION	
Setting	**W16**
X.24 CA signal	Pins 2 & 3 closed
V.24 104 signal	Pins 1 & 2 closed
No function	Open

PIN 4 CONFIGURATION	
Setting	**W19**
X.24 RA signal	Pins 2 & 3 closed
V.24 105 signal	Pins 1 & 2 closed
No function	Open

PIN 5 CONFIGURATION	
Setting	**W15**
X.24 IA signal	Pins 2 & 3 closed
V.24 106 signal	Pins 1 & 2 closed
No function	Open

PIN 6 CONFIGURATION	
Setting	**W18**
X.24 SA signal	Pins 2 & 3 closed
V.24 107 signal	Pins 1 & 2 closed
No function	Open

PIN 7 CONFIGURATION	
Setting	**W20**
X.24 BA signal	Pins 2 & 3 closed
V.24 102 signal	Pins 1 & 2 closed
No function	Open

PIN 8 CONFIGURATION	
Setting	**W17**
X.24 G signal	Pins 2 & 3 closed
V.24 109 signal	Pins 1 & 2 closed
No function	Open

PIN 15 CONFIGURATION	
Setting	**W2**
X.24 CB signal	Pins 2 & 3 closed
V.24 114 signal	Pins 1 & 2 closed
No function	Open

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PIN 17 CONFIGURATION	
Setting	**W8**
X.24 IB signal	Pins 2 & 3 closed
V.24 115 signal	Pins 1 & 2 closed
No function	Open

PIN 18 CONFIGURATION	
Setting	**W9**
X.24 SB signal	Pins 2 & 3 closed
V.24 141 signal	Pins 1 & 2 closed
No function	Open

DIAGNOSTIC LED(S)			
LED	**Color**	**Status**	**Condition**
LED	Unidentified	On	Self-test is in progress or self-test failed
LED	Unidentified	Off	Self-test passed
Note: The LED should go on when the board is first powered up. If the LED does not go off after a few moments, the self-test has failed.			