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Card Type	Serial
Chipset	Startech ST16C550
I/O Options	Serial ports (2)
Data Bus	16-bit ISA
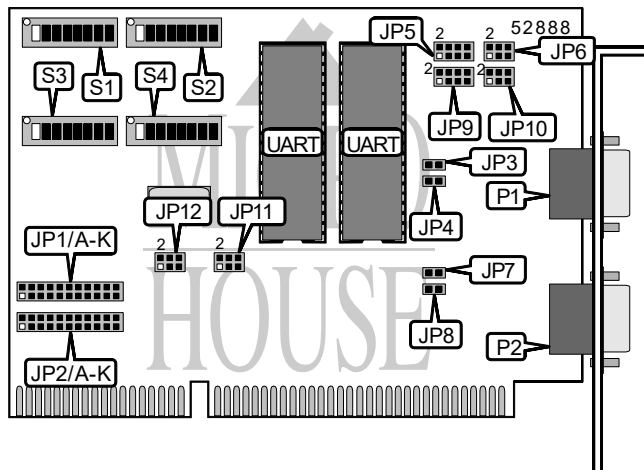
CONNECTIONS			
Function	**Label**	**Function**	**Label**
RS-422/485 serial port 1	P1	RS-422/485 serial port 2	P2

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Serial port 1 receive termination enabled	JP3	Closed
Serial port 1 receive termination disabled	JP3	Open
í Serial port 1 AUXIN termination enabled	JP4	Closed
Serial port 1 AUXIN termination disabled	JP4	Open
í Serial port 2 receive termination enabled	JP7	Closed
Serial port 2 receive termination disabled	JP7	Open
í Serial port 2 AUXIN termination enabled	JP8	Closed
Serial port 2 AUXIN termination disabled	JP8	Open
í Serial port 1 interrupt normal	JP11	Pins 1 & 3, 2 & 4 closed
Serial port 1 interrupt shared	JP11	Pins 3 & 5, 4 & 6 closed
í Serial port 2 interrupt normal	JP12	Pins 1 & 3, 2 & 4 closed
Serial port 2 interrupt shared	JP12	Pins 3 & 5, 4 & 6 closed
í Factory configured - do not alter	SW2/6	N/A
í Factory configured - do not alter	SW2/7	N/A
í Serial port 1 enabled	SW2/8	On
Serial port 1 disabled	SW2/8	Off
í Factory configured - do not alter	SW4/6	N/A
í Factory configured - do not alter	SW4/7	N/A
í Serial port 2 enabled	SW4/8	On
Serial port 2 disabled	SW4/8	Off

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SERIAL PORT 1 ADDRESS SELECTION						
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	On
0010h	On	On	On	On	On	On
0018h	On	On	On	On	On	On
0020h	On	On	On	On	On	On
í 03E8h	On	On	On	On	On	On
FFD8h	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off

SERIAL PORT 1 ADDRESS SELECTION (CON'T)						
Setting	**SW1/7**	**SW1/8**	**SW2/1**	**SW2/2**	**SW2/3**	**SW2/4**	**SW2/5**
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	Off
0010h	On	On	On	On	On	Off	On
0018h	On	On	On	On	On	Off	Off
0020h	On	On	On	On	Off	On	On
í 03E8h	Off	Off	Off	Off	Off	On	Off
FFD8h	Off	Off	Off	Off	On	Off	Off
FFE0h	Off	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off	Off

Note: A total of 16384 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW2/3 is the Least Significant Bit. The switches have the following decimal values: SW1/1=32768, SW1/2=16384, SW1/3=8192, SW1/4=4096, SW1/5=2048, SW1/6=1024, SW1/7=512, SW1/8=256, SW2/1=128, SW2/2=64, SW2/3=32, SW2/4=16, SW2/5=8. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

SERIAL PORT 2 ADDRESS SELECTION						
Setting	**SW3/1**	**SW3/2**	**SW3/3**	**SW3/4**	**SW3/5**	**SW3/6**
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	On
0010h	On	On	On	On	On	On
0018h	On	On	On	On	On	On
0020h	On	On	On	On	On	On
í 02E8h	On	On	On	On	On	On
FFD8h	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off

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SERIAL PORT 2 ADDRESS SELECTION (CON'T)							
Setting	**SW3/7**	**SW3/8**	**SW4/1**	**SW4/2**	**SW4/3**	**SW4/4**	**SW4/5**
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	Off
0010h	On	On	On	On	On	Off	On
0018h	On	On	On	On	On	Off	Off
0020h	On	On	On	On	Off	On	On
í 02E8h	Off	On	Off	Off	Off	On	Off
FFD8h	Off	Off	Off	Off	On	Off	Off
FFE0h	Off	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off	Off

Note: A total of 16384 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW3/1 is the Most Significant Bit and switch SW4/3 is the Least Significant Bit. The switches have the following decimal values: SW3/1=32768, SW3/2=16384, SW3/3=8192, SW3/4=4096, SW3/5=2048, SW3/6=1024, SW3/7=512, SW3/8=256, SW4/1=128, SW4/2=64, SW4/3=32, SW4/4=16, SW4/5=8. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

SERIAL PORT 1 INTERRUPT SELECTION					
Setting	**JP1/A**	**JP1/B**	**JP1/C**	**JP1/D**	**JP1/E**
IRQ2/9	Open	Open	Open	Open	Open
IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
í IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Closed
IRQ11	Open	Open	Open	Closed	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Closed	Open	Open	Open
IRQ15	Closed	Open	Open	Open	Open
Disabled	Open	Open	Open	Open	Open

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SERIAL PORT 1 INTERRUPT SELECTION (CON'T)						
Setting	JP1/F	JP1/G	JP1/H	JP1/I	JP1/J	JP1/K
IRQ2/9	Open	Open	Open	Open	Open	Closed
IRQ3	Open	Open	Open	Open	Closed	Open
IRQ4	Open	Open	Open	Closed	Open	Open
í IRQ5	Open	Open	Closed	Open	Open	Open
IRQ6	Open	Closed	Open	Open	Open	Open
IRQ7	Closed	Open	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open
Disabled	Open	Open	Open	Open	Open	Open

SERIAL PORT 2 INTERRUPT SELECTION					
Setting	JP2/A	JP2/B	JP2/C	JP2/D	JP2/E
IRQ2/9	Open	Open	Open	Open	Open
IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
í IRQ10	Open	Open	Open	Open	Closed
IRQ11	Open	Open	Open	Closed	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Closed	Open	Open	Open
IRQ15	Closed	Open	Open	Open	Open
Disabled	Open	Open	Open	Open	Open

SERIAL PORT 2 INTERRUPT SELECTION (CON'T)						
Setting	JP2/F	JP2/G	JP2/H	JP2/I	JP2/J	JP2/K
IRQ2/9	Open	Open	Open	Open	Open	Closed
IRQ3	Open	Open	Open	Open	Closed	Open
IRQ4	Open	Open	Open	Closed	Open	Open
IRQ5	Open	Open	Closed	Open	Open	Open
IRQ6	Open	Closed	Open	Open	Open	Open
IRQ7	Closed	Open	Open	Open	Open	Open
í IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open
Disabled	Open	Open	Open	Open	Open	Open

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SERIAL PORT 1 DRIVER CONFIGURATION	
Setting	**JP5**
í Driver always enabled	Pins 2 & 4 closed
Driver enabled on high DTR	Pins 1 & 2 closed
Driver enabled on low DTR	Pins 3 & 4 closed
Driver enabled on high RTS	Pins 5 & 6 closed
Driver enabled on low RTS	Pins 7 & 8 closed

SERIAL PORT 2 DRIVER CONFIGURATION	
Setting	**JP9**
í Driver always enabled	Pins 2 & 4 closed
Driver enabled on high DTR	Pins 1 & 2 closed
Driver enabled on low DTR	Pins 3 & 4 closed
Driver enabled on high RTS	Pins 5 & 6 closed
Driver enabled on low RTS	Pins 7 & 8 closed

SERIAL PORT 1 SIGNAL SELECTION	
Setting	**JP6**
í RTS looped to CTS, AUXIN looped to AUXOUT, clock in looped to clock out	Pins 1 & 2, 3 & 4, 5 & 6 closed
AUXOUT is RTS signal, AUXIN is CTS signal, clock in looped to clock out	Pins 1 & 3, 2 & 4, 5 & 6 closed
AUXOUT is clock out, AUXIN is clock in, RTS looped to CTS	Pins 1 & 2, 3 & 5, 4 & 6 closed

SERIAL PORT 2 SIGNAL SELECTION	
Setting	**JP10**
í RTS looped to CTS, AUXIN looped to AUXOUT, clock in looped to clock out	Pins 1 & 2, 3 & 4, 5 & 6 closed
AUXOUT is RTS signal, AUXIN is CTS signal, clock in looped to clock out	Pins 1 & 3, 2 & 4, 5 & 6 closed
AUXOUT is clock out, AUXIN is clock in, RTS looped to CTS	Pins 1 & 2, 3 & 5, 4 & 6 closed