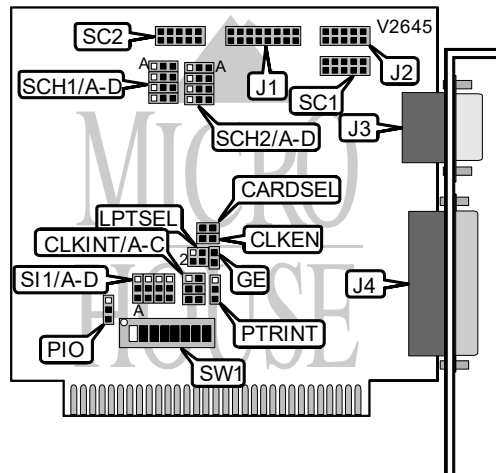


EVEREX SYSTEMS, INC.

MAGIC I/O EV170D (VER. 1.0)

Card Type	Multi-I/O card
Chip Set	Unidentified
Maximum Onboard Memory	None
I/O Options	Game port, parallel port, serial ports (2)
Data Bus	8-bit ISA



CONNECTIONS			
Function	Label	Function	Label
Game port	J1	Serial port 1	J3
Serial port 2	J2	Parallel port	J4

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í 8-bit IBM PC/XT slot disabled	CARDSE L	Unidentified
8-bit IBM PC/XT slot enabled	CARDSE L	Unidentified
í Clock/calendar enabled	CLKEN	Closed
Clock/calendar disabled	CLKEN	Open
í Game port enabled	GE	Closed
Game port disabled	GE	Open
í Printer mode	PIO	Pins 2 & 3 closed
General purpose I/O	PIO	Pins 1 & 2 closed
í Factory configured - do not alter	SC1	N/A
í Factory configured - do not alter	SC2	N/A

PARALLEL PORT ADDRESS SELECTION	
Setting	LPTSEL
í LPT2 (278h)	Pins 1 & 2 closed
LPT3 (3BCh)	Pins 3 & 4 closed
Disabled	Open

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SERIAL PORT 1 ADDRESS SELECTION				
Setting	SW1/1	SW1/2	SW1/3	SW1/4
í COM1 (3F8h)	On	Off	Off	Off
COM2 (2F8h)	Off	On	Off	Off
COM3 (3E8h)	Off	Off	On	Off
COM4 (2E8h)	Off	Off	Off	On
Disabled	Off	Off	Off	Off

SERIAL PORT 2 ADDRESS SELECTION				
Setting	SW1/5	SW1/6	SW1/7	SW1/8
COM1 (3F8h)	On	Off	Off	Off
í COM2 (2F8h)	Off	On	Off	Off
COM3 (3E8h)	Off	Off	On	Off
COM4 (2E8h)	Off	Off	Off	On
Disabled	Off	Off	Off	Off

MAGIC I/O SELECTION			
Setting	CLKINT/A	CLKINT/B	CLKINT/C
IRQ2	Open	Closed	Open
IRQ5	Open	Closed	Open
IRQ7	Closed	Open	Open
í Disabled	Open	Open	Open

SERIAL PORT 1 INTERRUPT SELECTION				
IRQ	SI1/A	SI1/B	SI1/C	SI1/D
IRQ2	Open	Open	Open	Pins 1 & 2 closed
IRQ3	Pins 1 & 2 closed	Open	Open	Open
í IRQ4	Open	Pins 1 & 2 closed	Open	Open
IRQ5	Open	Open	Pins 1 & 2 closed	Open

SERIAL PORT 2 INTERRUPT SELECTION				
IRQ	SI1/A	SI1/B	SI1/C	SI1/D
IRQ2	Open	Open	Open	Pins 2 & 3 closed
IRQ3	Pins 2 & 3 closed	Open	Open	Open
í IRQ4	Open	Pins 2 & 3 closed	Open	Open
IRQ5	Open	Open	Pins 2 & 3 closed	Open

FORCE-TRUES PORT 1 CONFIGURATION				
Setting	SCH1/A	SCH1/B	SCH1/C	SCH1/D
DSR	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
CTS	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
DCD	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
RI	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
Note: The above function disables the force-true options.				

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FORCE-TRUES PORT 2 CONFIGURATION				
Setting	SCH2/A	SCH2/B	SCH2/C	SCH2/D
DSR	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
CTS	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
DCD	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
RI	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
Note: The above function disables the force-true options.				

PARALLEL PORT INTERRUPT SELECTION	
IRQ	PTRINT
i IRQ7	Pins 1 & 2 closed
IRQ5	Pins 2 & 3 closed