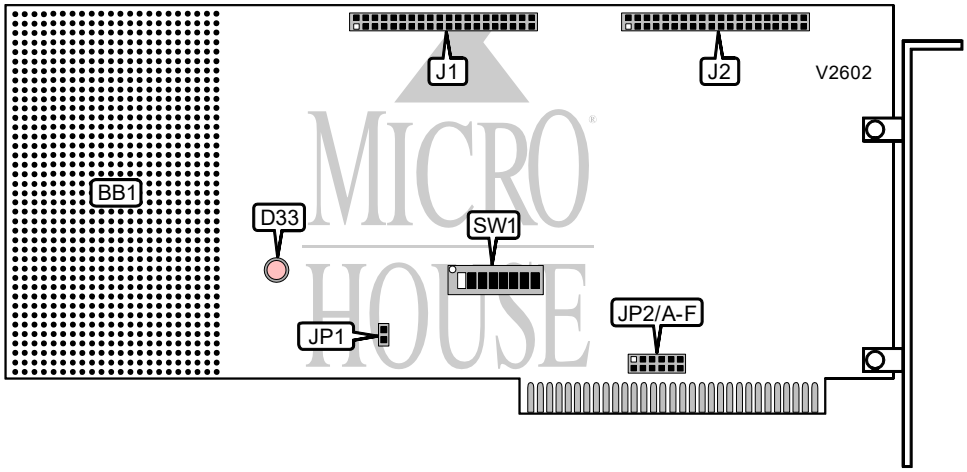
DECISION COMPUTER INTERNATIONAL CO., LTD.
16 CHANNEL RELAY OUTPUT/PHOTO-ISOLATOR

Card Type	Data acquisition
Chip Set	NEC 8255
I/O Options	Opto-isolated input port, relay control output port
Data Bus	8-bit ISA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Breadboard	BB1	Relay control output (see pinout below)	J2
Opto-isolated input (see pinout below)	J1		

J1 PINOUT			
Function	**Pins**	**Function**	**Pins**
Channel 1 relay control	1 & 2	Channel 11 relay control	21 & 22
Channel 2 relay control	3 & 4	Channel 12 relay control	23 & 24
Channel 3 relay control	5 & 6	Channel 13 relay control	25 & 26
Channel 4 relay control	7 & 8	Channel 14 relay control	27 & 28
Channel 5 relay control	9 & 10	Channel 15 relay control	29 & 30
Channel 6 relay control	11 & 12	Channel 16 relay control	31 & 32
Channel 7 relay control	13 & 14	Ground	33 & 34
Channel 8 relay control	15 & 16	+5V DC power	35 & 36
Channel 9 relay control	17 & 18	+12V DC power	37 & 38
Channel 10 relay control	19 & 20	Ground	39 & 40

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J2 PINOUT			
Function	**Pin**	**Function**	**Pin**
Channel 1 negative	1	Channel 11 negative	21
Channel 1 positive	2	Channel 11 positive	22
Channel 2 negative	3	Channel 12 negative	23
Channel 2 positive	4	Channel 12 positive	24
Channel 3 negative	5	Channel 13 negative	25
Channel 3 positive	6	Channel 13 positive	26
Channel 4 negative	7	Channel 14 negative	27
Channel 4 positive	8	Channel 14 positive	28
Channel 5 negative	9	Channel 15 negative	29
Channel 5 positive	10	Channel 15 positive	30
Channel 6 negative	11	Channel 16 negative	31
Channel 6 positive	12	Channel 16 positive	32
Channel 7 negative	13	Ground	33
Channel 7 positive	14	Ground	34
Channel 8 negative	15	+5V DC power	35
Channel 8 positive	16	+5V DC power	36
Channel 9 negative	17	+12V DC power	37
Channel 9 positive	18	+12V DC power	38
Channel 10 negative	19	Ground	39
Channel 10 positive	20	Ground	40

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Factory configured - do not alter	JP1	Unidentified

INTERRUPT						
Setting	**JP2/A**	**JP2/B**	**JP2/C**	**JP2/D**	**JP2/E**	**JP2/F**
IRQ2	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed
Disabled	Open	Open	Open	Open	Open	Open

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BASE I/O ADDRESS								
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**	**SW1/7**	**SW1/8**
000h	On	On	On	On	On	On	On	On
004h	Off	On	On	On	On	On	On	On
008h	On	Off	On	On	On	On	On	On
00Ch	Off	Off	On	On	On	On	On	On
010h	On	On	Off	On	On	On	On	On
í 1A0h	On	Off	Off	On	Off	On	On	On
3ECh	Off	Off	On	Off	Off	Off	Off	Off
3F0h	On	On	Off	Off	Off	Off	Off	Off
3F4h	Off	On	Off	Off	Off	Off	Off	Off
3F8h	On	Off	Off	Off	Off	Off	Off	Off
3FCh	Off	Off	Off	Off	Off	Off	Off	Off

Note: A total of 255 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW1/8 is the Least Significant Bit. The switches have the following decimal values: SW1/1=512, SW1/2=256, SW1/3=128, SW1/4=64, SW1/5=32, SW1/6=16, SW1/7=8, SW1/8=4. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

DIAGNOSTIC LED(S)
The function of the LED D33 is unidentified.