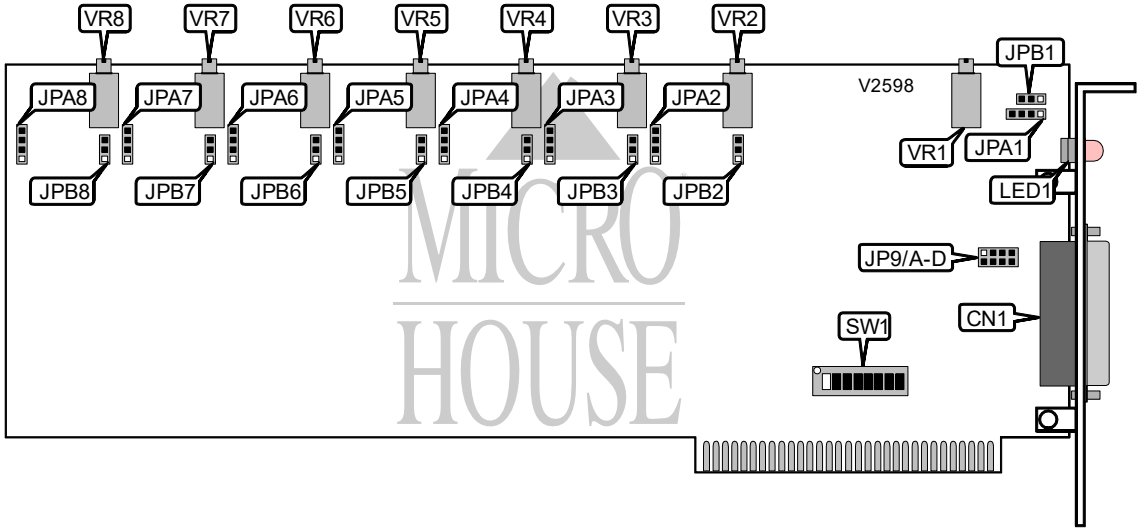
DECISION COMPUTER INTERNATIONAL CO., LTD.
8 CHANNEL 8-BIT D/A CARD

Card Type	Data acquisition
Chipset	Signal Processing Technologies
I/O Options	Analog/digital I/O
Data Bus	8-bit ISA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Analog/digital I/O (see pinout below)	CN1	Channel 5 reference voltage	VR5
Channel 1 reference voltage	VR1	Channel 6 reference voltage	VR6
Channel 2 reference voltage	VR2	Channel 7 reference voltage	VR7
Channel 3 reference voltage	VR3	Channel 8 reference voltage	VR8
Channel 4 reference voltage	VR4		

J1 PINOUT			
Function	**Pin**	**Function**	**Pin**
+12V DC power	1	-12V DC power	14
Not used	2	Not used	15
Ground	3	Analog-to-digital channel 8 current	16
Analog-to-digital channel 8 voltage	4	Analog-to-digital channel 7 current	17
Analog-to-digital channel 7 voltage	5	Analog-to-digital channel 6 current	18
Analog-to-digital channel 6 voltage	6	Analog-to-digital channel 5 current	19
Analog-to-digital channel 5 voltage	7	Analog-to-digital channel 4 current	20
Analog-to-digital channel 4 voltage	8	Analog-to-digital channel 3 current	21
Analog-to-digital channel 3 voltage	9	Analog-to-digital channel 2 current	22
Analog-to-digital channel 2 voltage	10	Analog-to-digital channel 1 current	23
Analog-to-digital channel 1 voltage	11	Ground	24
Ground	12	-5V DC power	25
+5V DC power	13		

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USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Factory configured - do not alter	SW1/7	Off
í Factory configured - do not alter	SW1/8	Off
í Factory configured - do not alter	JPA1	Open
í Factory configured - do not alter	JPA2	Open
í Factory configured - do not alter	JPA3	Open
í Factory configured - do not alter	JPA4	Open
í Factory configured - do not alter	JPA5	Open
í Factory configured - do not alter	JPA6	Open
í Factory configured - do not alter	JPA7	Open
í Factory configured - do not alter	JPA8	Open
í Channel 1 voltage is 0V to 9V	JPB1	Pins 1 & 2 closed
Channel 1 voltage is -9V to 9V	JPB1	Pins 2 & 3 closed
í Channel 2 voltage is 0V to 9V	JPB2	Pins 1 & 2 closed
Channel 2 voltage is -9V to 9V	JPB2	Pins 2 & 3 closed
í Channel 3 voltage is 0V to 9V	JPB3	Pins 1 & 2 closed
Channel 3 voltage is -9V to 9V	JPB3	Pins 2 & 3 closed
í Channel 4 voltage is 0V to 9V	JPB4	Pins 1 & 2 closed
Channel 4 voltage is -9V to 9V	JPB4	Pins 2 & 3 closed
í Channel 5 voltage is 0V to 9V	JPB5	Pins 1 & 2 closed
Channel 5 voltage is -9V to 9V	JPB5	Pins 2 & 3 closed
í Channel 6 voltage is 0V to 9V	JPB6	Pins 1 & 2 closed
Channel 6 voltage is -9V to 9V	JPB6	Pins 2 & 3 closed
í Channel 7 voltage is 0V to 9V	JPB7	Pins 1 & 2 closed
Channel 7 voltage is -9V to 9V	JPB7	Pins 2 & 3 closed
í Channel 8 voltage is 0V to 9V	JPB8	Pins 1 & 2 closed
Channel 8 voltage is -9V to 9V	JPB8	Pins 2 & 3 closed

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BASE I/O ADDRESS						
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
000h	On	On	On	On	On	On
010h	On	On	On	On	On	Off
020h	On	On	On	On	Off	On
030h	On	On	On	On	Off	Off
040h	On	On	On	Off	On	On
í 2B0h	Off	On	Off	On	Off	Off
3B0h	Off	Off	Off	On	Off	Off
3C0h	Off	Off	Off	Off	On	On
3D0h	Off	Off	Off	Off	On	Off
3E0h	Off	Off	Off	Off	Off	On
3F0h	Off	Off	Off	Off	Off	Off

Note: A total of 64 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW1/6 is the Least Significant Bit. The switches have the following decimal values: SW1/1=512, SW1/2=256, SW1/3=128, SW1/4=64, SW1/5=32, SW1/6=16. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

WAIT STATES				
Setting	**JP9/A**	**JP9/B**	**JP9/C**	**JP9/D**
0	Closed	Open	Open	Open
1	Open	Closed	Open	Open
2	Open	Open	Closed	Open
3	Open	Open	Open	Closed

DIAGNOSTIC LED(S)
The function of the LED is unidentified.