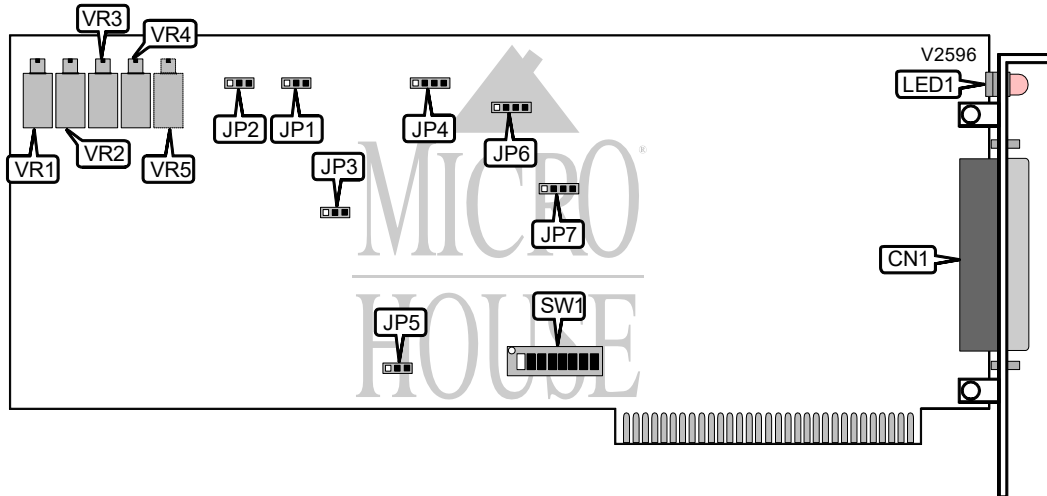


DECISION COMPUTER INTERNATIONAL CO., LTD.

12/14/16-BIT A/D-D/A CARD

Card Type
Chipset
I/O Options
Data Bus

Data acquisition
 Signal Processing Technologies HADC674Z
 Analog/digital I/O
 8-bit ISA



CONNECTIONS			
Function	Label	Function	Label
Analog/digital I/O (see pinout below)	CN1	Analog-to-digital voltage	VR3
Digital-to-analog channel 0 voltage	VR1	Analog-to-digital sampling and hold	VR4
Digital-to-analog channel 1 voltage	VR2	Analog-to-digital gain	VR5
Note: VR5 may not be present on all boards.			

J1 PINOUT			
Function	Pin	Function	Pin
Analog-to-digital negative channel 0	1	Analog-to-digital positive channel 0	20
Analog-to-digital negative channel 1	2	Analog-to-digital positive channel 1	21
Analog-to-digital negative channel 2	3	Analog-to-digital positive channel 2	22
Analog-to-digital negative channel 3	4	Analog-to-digital positive channel 3	23
Analog-to-digital negative channel 4	5	Analog-to-digital positive channel 4	24
Analog-to-digital negative channel 5	6	Analog-to-digital positive channel 5	25
Analog-to-digital negative channel 6	7	Analog-to-digital positive channel 6	26
Analog-to-digital negative channel 7	8	Analog-to-digital positive channel 7	27
Analog-to-digital negative channel 8	9	Analog-to-digital positive channel 8	28
Analog-to-digital negative channel 9	10	Analog-to-digital positive channel 9	29
Analog-to-digital negative channel 10	11	Analog-to-digital positive channel 10	30
Analog-to-digital negative channel 11	12	Analog-to-digital positive channel 11	31
Analog-to-digital negative channel 12	13	Analog-to-digital positive channel 12	32
Analog-to-digital negative channel 13	14	Analog-to-digital positive channel 13	33
Analog-to-digital negative channel 14	15	Analog-to-digital positive channel 14	34
Analog-to-digital negative channel 15	16	Analog-to-digital positive channel 15	35
Channel 1 voltage measurement	17	Channel 1 current measurement	36
Channel 0 voltage measurement	18	Channel 0 current measurement	37
Voltage/current measurement common	19		

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12/14/16-BIT A/D-D/A CARD

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USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í Factory configured - do not alter	JP3	Unidentified
í Analog-to-digital voltage is 0V to 10V	JP4	Pins 3 & 4 closed
Analog-to-digital voltage is -10V to 10V	JP4	Pins 1 & 2 closed
í Digital-to-analog channel 0 voltage is 0V to 10V	JP6	Pins 3 & 4 closed
Digital-to-analog channel 0 voltage is -10V to 10V	JP6	Pins 1 & 2 closed
í Digital-to-analog channel 1 voltage is 0V to 10V	JP7	Pins 3 & 4 closed
Digital-to-analog channel 1 voltage is -10V to 10V	JP7	Pins 1 & 2 closed
í Factory configured - do not alter	SW1/8	On

SAMPLING AND HOLD VOLTAGE			
Setting	JP1	JP2	JP5
í Fixed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
Set by VR4	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

BASE I/O ADDRESS							
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
000h	On	On	On	On	On	On	On
008h	On	On	On	On	On	On	Off
010h	On	On	On	On	On	Off	On
018h	On	On	On	On	On	Off	Off
020h	On	On	On	On	Off	On	On
3D8h	Off	Off	Off	Off	On	Off	Off
3E0h	Off	Off	Off	Off	Off	On	On
3E8h	Off	Off	Off	Off	Off	On	Off
3F0h	Off	Off	Off	Off	Off	Off	On
3F8h	Off	Off	Off	Off	Off	Off	Off
Note: A total of 128 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW1/7 is the Least Significant Bit. The switches have the following decimal values: SW1/1=512, SW1/2=256, SW1/3=128, SW1/4=64, SW1/5=32, SW1/6=16, SW1/7=8. Turn off the switches and add the values of the switches that are off to obtain the correct address. (Off=1, On=0)							

DIAGNOSTIC LED(S)
The function of the LED is unidentified.