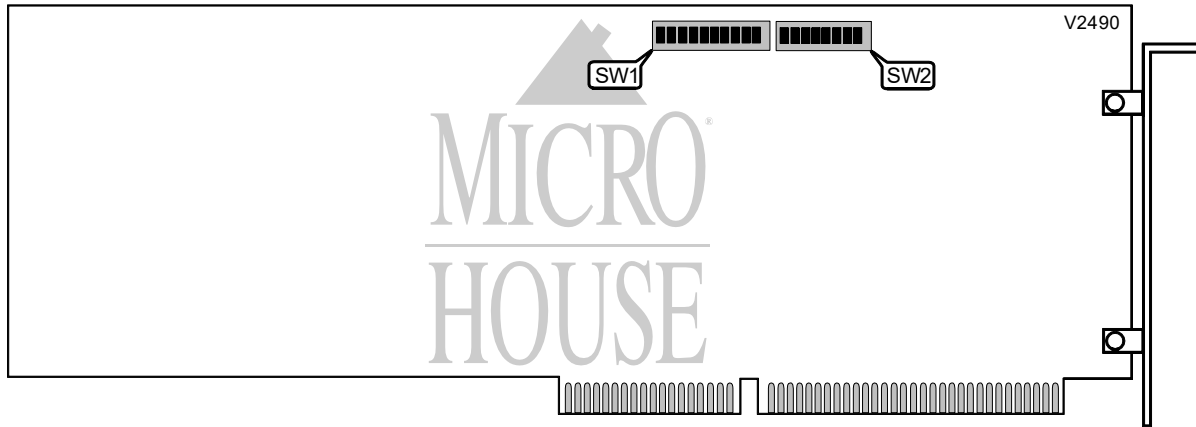


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RAMPAGE AT (202079-001)

Card Type	Memory card
Chip Set	Unidentified
Maximum Onboard Memory	2MB RAM (Type unidentified)
I/O Options	None
Data Bus	16-bit ISA



USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Dual page mode enabled	SW1/9	On
Dual page mode disabled	SW1/9	Off
í Factory configured - do not alter	SW1/10	N/A
í Factory configured - do not alter (memory priority check)	SW2/8	Unidentified

NON-EXPANDED MEMORY SWITCH CONFIGURATION				
Size	SW1/1	SW1/2	SW1/3	SW1/4
í 128KB	On	On	On	On
256KB	On	On	On	Off
384KB	On	On	Off	On
512KB	On	On	Off	Off
640KB	On	Off	On	On
768KB	On	Off	On	Off
896KB	On	Off	Off	On
1024KB	On	Off	Off	Off
1152KB	Off	On	On	On
1280KB	Off	On	On	Off
1408KB	Off	On	Off	On
1536KB	Off	On	Off	Off
1664KB	Off	Off	On	On
1792KB	Off	Off	On	Off
1920KB	Off	Off	Off	On
2048KB	Off	Off	Off	Off

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STARTING MEMORY ADDRESS SELECTION							
Address	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7
0KB	On	On	On	On	On	On	On
128KB	On	On	On	On	On	On	Off
256KB	On	On	On	On	On	Off	On
384KB	On	On	On	On	On	Off	Off
512KB	On	On	On	On	Off	On	On
12MB	Off	Off	On	On	On	On	On
13MB	Off	Off	On	Off	On	On	On
14MB	Off	Off	Off	On	On	On	On
15MB	Off	Off	Off	Off	On	On	On
All Rampage	Off	Off	Off	Off	Off	Off	Off

Note: The Rampage AT may be set for any memory address from 0K to 15896K. Each switch represents a quantity of memory. SW2/1 is the Most Significant Bit and SW2/7 is the Least Significant Bit. The switches have the following values: SW2/1=8192K, SW2/2=4096K, SW2/3=2048K, SW2/4=1024K, SW2/5=512K, SW2/6=256K, and SW2/7=128K. Turn off the switches and add their values to obtain the correct starting memory address. (Off=1, On=0)

BASE I/O ADDRESS SELECTION				
Setting	SW1/5	SW1/6	SW1/7	SW1/8
208h	On	On	On	On
218h	On	On	On	Off
228h	On	On	Off	On
238h	On	On	Off	Off
248h	On	Off	On	On
258h	On	Off	On	Off
268h	On	Off	Off	On
278h	On	Off	Off	Off
288h	Off	On	On	On
298h	Off	On	On	Off
2A8h	Off	On	Off	On
2B8h	Off	On	Off	Off
2C8h	Off	Off	On	On
2D8h	Off	Off	On	Off
2E8h	Off	Off	Off	On
2F8h	Off	Off	Off	Off

MISCELLANEOUS TECHNICAL NOTES
Note: The location and configuration of the memory is unidentified. The IBM model 339 AT does not support this board in the #20279-301 -A, -B, -C, or the #20279-302 - A revisions.