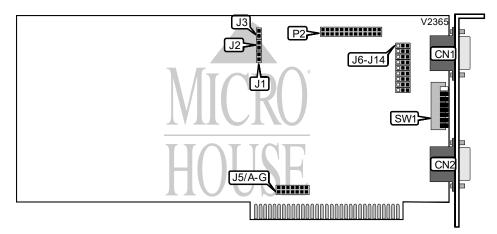
EVEREX SYSTEMS, INC. EV-673 (PWA-00271-00)

Card Type Video Chip Set Highest Resolution Supported Maximum Video Memory Data Bus Video Types Supported

Video card Unidentified Unidentified None 8-bit ISA Monochrome, CGA, EGA



CONNECTIONS					
Function	Label	Function	Label		
9-pin TTL monitor connector (Out)	CN1	Auxiliary video extension	P2		
15-pin analog video port	CN2				

USER CONFIGURABLE SETTINGS					
Function	Label	Position			
í Factory configured - do not alter	J1	Closed			
í EGA address is 3XXh	J2	Closed			
EGA address is 2XXh	J2	Open			
í Non maskable interrupt	J3	Closed			
Interrupt selected	J3	Open			
í Hercules emulation disabled	SW1/5	Off			
Hercules emulation enabled	SW1/5	On			
í Output signal is TTL	SW1/6	Off			
Output signal is analog	SW1/6	On			

INTERRUPT SELECTION							
IRQ	J5/A	J5/B	J6/C	J5/D	J5/E	J5/F	J5/G
2	Open	Open	Open	Open	Open	Closed	Open
3	Open	Open	Open	Open	Closed	Open	Open
4	Open	Open	Open	Closed	Open	Open	Open
5	Open	Open	Closed	Open	Open	Open	Open
6	Open	Closed	Open	Open	Open	Open	Open
7	Closed	Open	Open	Open	Open	Open	Open
í Disabled	Open	Open	Open	Open	Open	Open	Closed

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SIGNAL OUTPUT CONFIGURATION						
Setting	J6	J7	J8	J 9	J10	
Analog	Pins 1 & 2					
TTL	Pins 2 & 3					
Note: Pins designated are in the closed position.						

SIGNAL OUTPUT CONFIGURATION (CON'T)					
Setting	J11	J12	J13	J14	
Analog	Pins 1 & 2 closed				
TLL	Pins 2 & 3 closed				