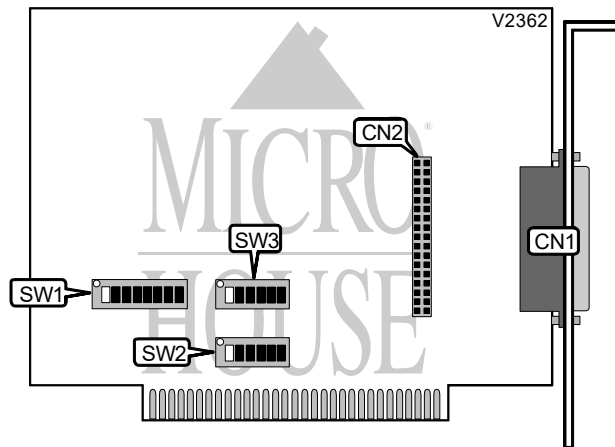


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Card Type	CD-ROM host card
Chip Set	Unidentified
Maximum Onboard Memory	None
I/O Options	Host bus connector (2)
Data Bus	8-bit ISA
Floppy drives supported	None



CONNECTIONS			
Function	Label	Function	Label
Internal host bus connector	CN1	External host bus connector	CN2

BASE I/O ADDRESS SELECTION								
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
000h	On	On	On	On	On	On	On	On
004h	Off	On	On	On	On	On	On	On
008h	On	Off	On	On	On	On	On	On
00Ch	Off	Off	On	On	On	On	On	On
010h	On	On	Off	On	On	On	On	On
340h	On	On	On	On	Off	On	Off	Off
3ECh	Off	Off	On	Off	Off	Off	Off	Off
3F0h	On	On	Off	Off	Off	Off	Off	Off
3F4h	Off	On	Off	Off	Off	Off	Off	Off
3F8h	On	Off	Off	Off	Off	Off	Off	Off
3FCh	Off	Off	Off	Off	Off	Off	Off	Off

Note: A total of 256 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/8 is the Most Significant Bit and switch SW1/1 is the Least Significant Bit. The switches have the following decimal values: SW1/8=512, SW1/7=256, SW1/6=128, SW1/5=64, SW1/4=32, SW1/3=16, SW1/2=8, SW1/1=4. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)

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INTERRUPT SELECTION						
IRQ	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6
2	On	Off	Off	Off	Off	Off
3	Off	On	Off	Off	Off	Off
4	Off	Off	On	Off	Off	Off
5	Off	Off	Off	On	Off	Off
í None	Off	Off	Off	Off	Off	Off

DMA CHANNEL SELECTION						
Channel	SW3/1	SW3/2	SW3/3	SW3/4	SW3/5	SW3/6
1	On	Off	Off	On	Off	Off
2	Off	On	Off	Off	On	Off
3	Off	Off	On	Off	Off	On
í None	Off	Off	Off	Off	Off	Off