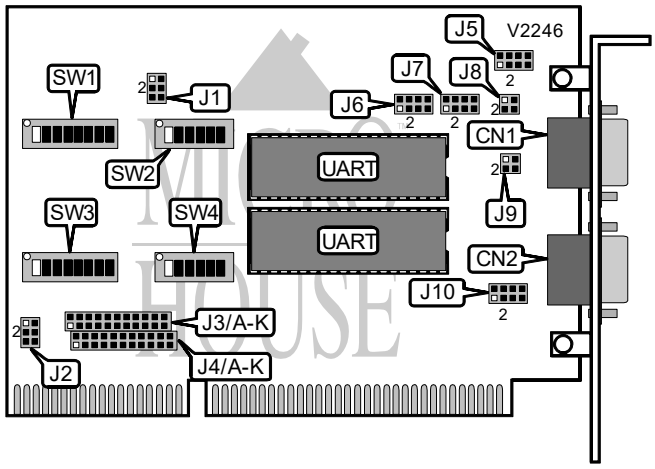
QUATECH, INC.
TV-200.5, TV-300.5

Card Type	Serial
Chip Set	Unidentified
I/O Options	Serial ports (2)
Data Bus	16-bit ISA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Serial port 1	CN1	Serial port 2	CN2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Port 1 interrupt sharing disabled	J2	Pins 1 & 2 closed
Port 1 interrupt sharing enabled	J2	Pins 2 & 3 closed
í Port 2 interrupt sharing disabled	J2	Pins 4 & 5 closed
Port 2 interrupt sharing enabled	J2	Pins 5 & 6 closed
í Factory configured - do not alter	SW2/6	N/A

SERIAL PORT 1 ADDRESS							
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**	**SW1/7**
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	On
0010h	On	On	On	On	On	On	On
0018h	On	On	On	On	On	On	On
0020h	On	On	On	On	On	On	On
02E8h (COM4:)	On	On	On	On	On	On	Off
02F8h (COM2:)	On	On	On	On	On	On	Off
03E8h (COM3:)	On	On	On	On	On	On	Off
í 03F8h (COM1:)	On	On	On	On	On	On	Off
FFD8h	Off	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off	Off

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SERIAL PORT 1 ADDRESS (CON'T)						
Setting	**SW1/8**	**SW2/1**	**SW2/2**	**SW2/3**	**SW2/4**	**SW2/5**
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	Off
0010h	On	On	On	On	Off	On
0018h	On	On	On	On	Off	Off
0020h	On	On	On	Off	On	On
02E8h (COM4:)	On	Off	Off	Off	On	Off
02F8h (COM2:)	On	Off	Off	Off	Off	Off
03E8h (COM3:)	Off	Off	Off	Off	On	Off
í 03F8h (COM1:)	Off	Off	Off	Off	Off	Off
FFD8h	Off	Off	Off	On	Off	Off
FFE0h	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off

Note: A total of 8192 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW2/5 is the Least Significant Bit. The switches have the following decimal values: SW1/1=32768, SW1/2=16384, SW1/3=8192, SW1/4=4096, SW1/5=2048, SW1/6=1024, SW1/7=512, SW1/8=256, SW2/1=128, SW2/2=64, SW2/3=32, SW2/4=16, SW2/5=8. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)

SERIAL PORT 2 ADDRESS							
Setting	**SW3/1**	**SW3/2**	**SW3/3**	**SW3/4**	**SW3/5**	**SW3/6**	**SW3/7**
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	On
0010h	On	On	On	On	On	On	On
0018h	On	On	On	On	On	On	On
0020h	On	On	On	On	On	On	On
02E8h (COM4:)	On	On	On	On	On	On	Off
í 02F8h (COM2:)	On	On	On	On	On	On	Off
03E8h (COM3:)	On	On	On	On	On	On	Off
03F8h (COM1:)	On	On	On	On	On	On	Off
FFD8h	Off	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off	Off

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SERIAL PORT 2 ADDRESS (CON'T)						
Setting	**SW3/8**	**SW4/1**	**SW4/2**	**SW4/3**	**SW4/4**	**SW4/5**
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	Off
0010h	On	On	On	On	Off	On
0018h	On	On	On	On	Off	Off
0020h	On	On	On	Off	On	On
02E8h (COM4:)	On	Off	Off	Off	On	Off
í 02F8h (COM2:)	On	Off	Off	Off	Off	Off
03E8h (COM3:)	Off	Off	Off	Off	On	Off
03F8h (COM1:)	Off	Off	Off	Off	Off	Off
FFD8h	Off	Off	Off	On	Off	Off
FFE0h	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off

Note: A total of 8192 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW3/1 is the Most Significant Bit and switch SW4/5 is the Least Significant Bit. The switches have the following decimal values: SW3/1=32768, SW3/2=16384, SW3/3=8192, SW3/4=4096, SW3/5=2048, SW3/6=1024, SW3/7=512, SW3/8=256, SW4/1=128, SW4/2=64, SW4/3=32, SW4/4=16, SW4/5=8. Turn off the switches and add the values of the switches to obtain the correct memory address. (Off=1, On=0)

SERIAL PORT 1 INTERRUPT						
Setting	**J3/A**	**J3/B**	**J3/C**	**J3/D**	**J3/E**	**J3/F**
IRQ2/9	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
í IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed
IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open

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SERIAL PORT 1 INTERRUPT (CON'T)					
Setting	**J3/G**	**J3/H**	**J3/I**	**J3/J**	**J3/K**
IRQ2/9	Open	Open	Open	Open	Open
IRQ3	Open	Open	Open	Open	Open
í IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Closed	Open	Open	Open	Open
IRQ11	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Closed

SERIAL PORT 2 INTERRUPT						
Setting	**J4/A**	**J4/B**	**J4/C**	**J4/D**	**J4/E**	**J4/F**
IRQ2/9	Closed	Open	Open	Open	Open	Open
í IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed
IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open

SERIAL PORT 2 INTERRUPT (CON'T)					
Setting	**J4/G**	**J4/H**	**J4/I**	**J4/J**	**J4/K**
IRQ2/9	Open	Open	Open	Open	Open
í IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Closed	Open	Open	Open	Open
IRQ11	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Closed

SERIAL PORT 1 TERMINATING RESISTORS	
Setting	**J8**
í Terminated	Pins 1 & 3, 2 & 4 closed
Not terminated	Open

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SERIAL PORT 2 TERMINATING RESISTORS	
Setting	**J9**
í Terminated	Pins 1 & 3, 2 & 4 closed
Not terminated	Open

SERIAL PORT 1 DUPLEX CONTROL	
Setting	**J6**
í Full duplex	Pins 5 & 6 closed
Half duplex, transmit on low DTR	Pins 1 & 5 closed
Half duplex, transmit on high DTR	Pins 2 & 6 closed
Half duplex, transmit on low RTS	Pins 3 & 7 closed
Half duplex, transmit on high RTS	Pins 4 & 8 closed

SERIAL PORT 2 DUPLEX CONTROL	
Setting	**J7**
í Full duplex	Pins 5 & 6 closed
Half duplex, transmit on low DTR	Pins 1 & 5 closed
Half duplex, transmit on high DTR	Pins 2 & 6 closed
Half duplex, transmit on low RTS	Pins 3 & 7 closed
Half duplex, transmit on high RTS	Pins 4 & 8 closed

SERIAL PORT 1 BUS MASTER/SLAVE CONTROL	
Setting	**J5**
í Slave device	Pins 1 & 5, 2 & 6, 3 & 7, 4 & 8 closed
Bus master device	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed

SERIAL PORT 2 BUS MASTER/SLAVE CONTROL	
Setting	**J10**
í Slave device	Pins 1 & 5, 2 & 6, 3 & 7, 4 & 8 closed
Bus master device	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed

BAUD CLOCK	
Setting	**J1**
í 1.8432MHz clock	Pins 1 & 2, 3 & 6, 4 & 5 closed
3.6864MHz clock	Pins 2 & 3, 4 & 5 closed
9.216MHz clock	Pins 1 & 2, 5 & 6 closed
18.432MHz clock	Pins 2 & 5 closed