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I M A G E   3 2   (V E R .   1)

Card Type	Video card
Video Chip Set	Hitachi HD63484
Highest Resolution Supported	1024 x 1024
Maximum Video Memory	4MB VRAM
Data Bus	8/16-bit ISA
Video Types Supported	XVGA
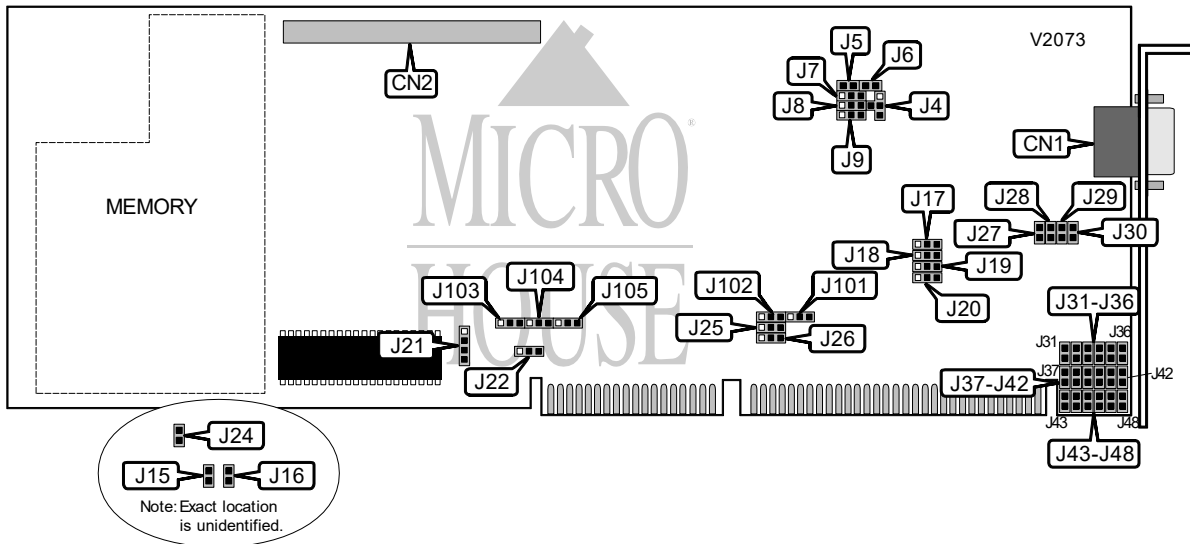
CONNECTIONS			
Function	**Label**	**Function**	**Label**
Video out	CN1	Memory daughterboard connector	CN2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J4	Unidentified
í Hitachi ACRTC operates in 8-bit transfer mode	J5	Closed
Hitachi ACRTC operates in 16-bit transfer mode	J5	Open
í Factory configured - do not alter	J6	Unidentified
í Composite sync mode enabled	J7	Pins 1 & 2 closed
VSync mode enabled	J7	Pins 2 & 3 closed
Composite sync on green signal enabled	J15	Closed
Composite sync on green signal disabled	J15	Open
Composite sync on red and blue signals enabled	J16	Closed
Composite sync on red and blue signals disabled	J16	Open
í Factory configured - do not alter	J21	Unidentified
í Negative HSync signal	J22	Pins 1 & 2 closed
Positive HSync signal	J22	Pins 2 & 3 closed
í CS16 I/O disabled	J24	Open
CS16 I/O enabled	J24	Closed

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DRAM CONFIGURATION	
Size	**CN2**
1MB	Not installed
4MB	Installed
Note: The first megabyte of memory is located on the board. Its exact location is unidentified. The three additional megabytes are located on the memory daughterboard.	

INTERRUPT					
Setting	**J101**	**J102**	**J103**	**J104**	**J105**
IRQ3	Pins 1 & 2 closed	Open	Open	Open	Open
IRQ4	Open	Pins 1 & 2 closed	Open	Open	Open
IRQ5	Open	Open	Pins 1 & 2 closed	Open	Open
IRQ6	Open	Open	Open	Pins 1 & 2 closed	Open
IRQ7	Open	Open	Open	Open	Pins 1 & 2 closed
IRQ10	Pins 2 & 3 closed	Open	Open	Open	Open
IRQ11	Open	Pins 2 & 3 closed	Open	Open	Open
IRQ12	Open	Open	Pins 2 & 3 closed	Open	Open
IRQ14	Open	Open	Open	Pins 2 & 3 closed	Open
IRQ15	Open	Open	Open	Open	Pins 2 & 3 closed

BASE I/O ADDRESS					
Setting	**J27**	**J28**	**J29**	**J30**	**J31**	**J32**
2A0h	Closed	Open	Closed	Open	Closed	Open
í 2B0h	Open	Open	Closed	Open	Closed	Open
2C0h	Closed	Closed	Open	Open	Closed	Open
2D0h	Open	Closed	Open	Open	Closed	Open
2E0h	Closed	Open	Open	Open	Closed	Open
2F0h	Open	Open	Open	Open	Closed	Open
3A0h	Closed	Open	Closed	Open	Open	Open
3B0h	Open	Open	Closed	Open	Open	Open
3C0h	Closed	Closed	Open	Open	Open	Open
3D0h	Open	Closed	Open	Open	Open	Open
3E0h	Closed	Open	Open	Open	Open	Open

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SHARED RAM ADDRESS (64KB PAGE SIZE, 8-BIT ISA INSTALLATION)							
Setting	**J8**	**J9**	**J17**	**J18**	**J19**	**J20**	**J33**
10000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
20000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
30000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
40000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
50000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
60000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
70000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
80000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
90000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
A0000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
B0000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
C0000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
í D0000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
E0000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Closed
Note: Pins designated are in the closed position.							

SHARED RAM ADDRESS (64KB PAGE SIZE, 8-BIT ISA INSTALLATION, CON'T)							
Setting	**J34**	**J35**	**J36**	**J37**	**J38**	**J39**	**J40**
10000h	Closed	Closed	Closed	Open	Open	Open	Open
20000h	Closed	Closed	Closed	Open	Open	Open	Open
30000h	Closed	Closed	Closed	Open	Open	Open	Open
40000h	Closed	Closed	Closed	Open	Open	Open	Open
50000h	Closed	Closed	Closed	Open	Open	Open	Open
60000h	Closed	Closed	Closed	Open	Open	Open	Open
70000h	Closed	Closed	Closed	Open	Open	Open	Open
80000h	Closed	Closed	Closed	Open	Open	Open	Open
90000h	Closed	Closed	Closed	Open	Open	Open	Open
A0000h	Closed	Closed	Closed	Open	Open	Open	Open
B0000h	Closed	Closed	Closed	Open	Open	Open	Open
C0000h	Closed	Closed	Closed	Open	Open	Open	Open
í D0000h	Closed	Closed	Closed	Open	Open	Open	Open
E0000h	Closed	Closed	Closed	Open	Open	Open	Open

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IMAGE 32 (VER. 1)

SHARED RAM ADDRESS (64KB PAGE SIZE, 8-BIT ISA INSTALLATION, CON'T)							
Setting	J41	J42	J43	J44	J45	J46	J47	J48
10000h	Open	Closed	Closed	Closed	Open	Open	Open	Open
20000h	Closed	Open	Closed	Closed	Open	Open	Open	Open
30000h	Open	Open	Closed	Closed	Open	Open	Open	Open
40000h	Closed	Closed	Open	Closed	Open	Open	Open	Open
50000h	Open	Closed	Open	Closed	Open	Open	Open	Open
60000h	Closed	Open	Open	Closed	Open	Open	Open	Open
70000h	Open	Open	Open	Closed	Open	Open	Open	Open
80000h	Closed	Closed	Closed	Open	Open	Open	Open	Open
90000h	Open	Closed	Closed	Open	Open	Open	Open	Open
A0000h	Closed	Open	Closed	Open	Open	Open	Open	Open
B0000h	Open	Open	Closed	Open	Open	Open	Open	Open
C0000h	Closed	Closed	Open	Open	Open	Open	Open	Open
í D0000h	Open	Closed	Open	Open	Open	Open	Open	Open
E0000h	Closed	Open	Open	Open	Open	Open	Open	Open

SHARED RAM ADDRESS (64KB PAGE SIZE, 16-BIT ISA INSTALLATION)							
Setting	J8	J9	J17	J18	J19	J20	J33
100000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
200000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
300000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
400000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
500000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
600000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
700000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
800000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
900000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
A00000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
B00000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
C00000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
D00000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
E00000h	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Open
Note: Pins designated are in the closed position.							

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SHARED RAM ADDRESS (64KB PAGE SIZE, 16-BIT ISA INSTALLATION, CON'T)							
Setting	**J34**	**J35**	**J36**	**J37**	**J38**	**J39**	**J40**
100000h	Open	Open	Open	Closed	Closed	Closed	Closed
200000h	Open	Open	Open	Closed	Closed	Closed	Closed
300000h	Open	Open	Open	Closed	Closed	Closed	Closed
400000h	Open	Open	Open	Closed	Closed	Closed	Closed
500000h	Open	Open	Open	Closed	Closed	Closed	Closed
600000h	Open	Open	Open	Closed	Closed	Closed	Closed
700000h	Open	Open	Open	Closed	Closed	Closed	Closed
800000h	Open	Open	Open	Closed	Closed	Closed	Closed
900000h	Open	Open	Open	Closed	Closed	Closed	Closed
A00000h	Open	Open	Open	Closed	Closed	Closed	Closed
B00000h	Open	Open	Open	Closed	Closed	Closed	Closed
C00000h	Open	Open	Open	Closed	Closed	Closed	Closed
D00000h	Open	Open	Open	Closed	Closed	Closed	Closed
E00000h	Open	Open	Open	Closed	Closed	Closed	Closed

SHARED RAM ADDRESS (64KB PAGE SIZE, 16-BIT ISA INSTALLATION, CON'T)							
Setting	**J41**	**J42**	**J43**	**J44**	**J45**	**J46**	**J47**	**J48**
100000h	Open	Open	Open	Open	Open	Closed	Closed	Closed
200000h	Open	Open	Open	Open	Closed	Open	Closed	Closed
300000h	Open	Open	Open	Open	Open	Open	Closed	Closed
400000h	Open	Open	Open	Open	Closed	Closed	Open	Closed
500000h	Open	Open	Open	Open	Open	Closed	Open	Closed
600000h	Open	Open	Open	Open	Closed	Open	Open	Closed
700000h	Open	Open	Open	Open	Open	Open	Open	Closed
800000h	Open	Open	Open	Open	Closed	Closed	Closed	Open
900000h	Open	Open	Open	Open	Open	Closed	Closed	Open
A00000h	Open	Open	Open	Open	Closed	Open	Closed	Open
B00000h	Open	Open	Open	Open	Open	Open	Closed	Open
C00000h	Open	Open	Open	Open	Closed	Closed	Open	Open
D00000h	Open	Open	Open	Open	Open	Closed	Open	Open
E00000h	Open	Open	Open	Open	Closed	Open	Open	Open

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SHARED RAM ADDRESS (DIRECT ADDRESSING, 1MB PAGE SIZE)							
Setting	**J8**	**J9**	**J17**	**J18**	**J19**	**J20**	**J33**
100000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
200000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
300000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
400000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
500000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
600000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
700000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
800000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
900000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
A00000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
B00000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
C00000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
D00000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
E00000h	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
Note: Pins designated are in the closed position.							

SHARED RAM ADDRESS (DIRECT ADDRESSING, 1MB PAGE SIZE, CON'T)							
Setting	**J34**	**J35**	**J36**	**J37**	**J38**	**J39**	**J40**
100000h	Open	Open	Open	Closed	Closed	Closed	Closed
200000h	Open	Open	Open	Closed	Closed	Closed	Closed
300000h	Open	Open	Open	Closed	Closed	Closed	Closed
400000h	Open	Open	Open	Closed	Closed	Closed	Closed
500000h	Open	Open	Open	Closed	Closed	Closed	Closed
600000h	Open	Open	Open	Closed	Closed	Closed	Closed
700000h	Open	Open	Open	Closed	Closed	Closed	Closed
800000h	Open	Open	Open	Closed	Closed	Closed	Closed
900000h	Open	Open	Open	Closed	Closed	Closed	Closed
A00000h	Open	Open	Open	Closed	Closed	Closed	Closed
B00000h	Open	Open	Open	Closed	Closed	Closed	Closed
C00000h	Open	Open	Open	Closed	Closed	Closed	Closed
D00000h	Open	Open	Open	Closed	Closed	Closed	Closed
E00000h	Open	Open	Open	Closed	Closed	Closed	Closed

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SHARED RAM ADDRESS (DIRECT ADDRESSING, 1MB PAGE SIZE, CON'T)							
Setting	J41	J42	J43	J44	J45	J46	J47	J48
100000h	Open	Open	Open	Open	Open	Closed	Closed	Closed
200000h	Open	Open	Open	Open	Closed	Open	Closed	Closed
300000h	Open	Open	Open	Open	Open	Open	Closed	Closed
400000h	Open	Open	Open	Open	Closed	Closed	Open	Closed
500000h	Open	Open	Open	Open	Open	Closed	Open	Closed
600000h	Open	Open	Open	Open	Closed	Open	Open	Closed
700000h	Open	Open	Open	Open	Open	Open	Open	Closed
800000h	Open	Open	Open	Open	Closed	Closed	Closed	Open
900000h	Open	Open	Open	Open	Open	Closed	Closed	Open
A00000h	Open	Open	Open	Open	Closed	Open	Closed	Open
B00000h	Open	Open	Open	Open	Open	Open	Closed	Open
C00000h	Open	Open	Open	Open	Closed	Closed	Open	Open
D00000h	Open	Open	Open	Open	Open	Closed	Open	Open
E00000h	Open	Open	Open	Open	Closed	Open	Open	Open

SHARED RAM ADDRESS (DIRECT ADDRESSING, 2MB PAGE SIZE)							
Setting	J8	J9	J17	J18	J19	J20	J33
200000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
400000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
600000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
800000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
A00000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
C00000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
F00000h	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
Note: Pins designated are in the closed position.							

SHARED RAM ADDRESS (DIRECT ADDRESSING, 2MB PAGE SIZE, CON'T)							
Setting	J34	J35	J36	J37	J38	J39	J40
200000h	Open	Open	Open	Open	Closed	Closed	Closed
400000h	Open	Open	Open	Open	Closed	Closed	Closed
600000h	Open	Open	Open	Open	Closed	Closed	Closed
800000h	Open	Open	Open	Open	Closed	Closed	Closed
A00000h	Open	Open	Open	Open	Closed	Closed	Closed
C00000h	Open	Open	Open	Open	Closed	Closed	Closed
F00000h	Open	Open	Open	Open	Closed	Closed	Closed

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SHARED RAM ADDRESS (DIRECT ADDRESSING, 2MB PAGE SIZE, CON'T)								
Setting	**J41**	**J42**	**J43**	**J44**	**J45**	**J46**	**J47**	**J48**
200000h	Open	Open	Open	Open	Open	Open	Closed	Closed
400000h	Open	Open	Open	Open	Open	Closed	Open	Closed
600000h	Open	Open	Open	Open	Open	Open	Open	Closed
800000h	Open	Open	Open	Open	Open	Closed	Closed	Open
A00000h	Open	Open	Open	Open	Open	Open	Closed	Open
C00000h	Open	Open	Open	Open	Open	Closed	Open	Open
F00000h	Open	Open	Open	Open	Open	Open	Open	Open

SHARED RAM ADDRESS (DIRECT ADDRESSING, 4MB PAGE SIZE)							
Setting	**J8**	**J9**	**J17**	**J18**	**J19**	**J20**	**J33**
400000h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
800000h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
C00000h	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Open
Note: Pins designated are in the closed position.							

SHARED RAM ADDRESS (DIRECT ADDRESSING, 4MB PAGE SIZE, CON'T)							
Setting	**J34**	**J35**	**J36**	**J37**	**J38**	**J39**	**J40**
400000h	Open	Open	Open	Open	Open	Closed	Closed
800000h	Open	Open	Open	Open	Open	Closed	Closed
C00000h	Open	Open	Open	Open	Open	Closed	Closed

SHARED RAM ADDRESS (DIRECT ADDRESSING, 4MB PAGE SIZE, CON'T)								
Setting	**J41**	**J42**	**J43**	**J44**	**J45**	**J46**	**J47**	**J48**
400000h	Open	Open	Open	Open	Open	Open	Open	Closed
800000h	Open	Open	Open	Open	Open	Open	Closed	Open
C00000h	Open	Open	Open	Open	Open	Open	Open	Open

SLOT TYPE		
Setting	**J25**	**J26**
í 8-bit ISA	Pins 2 & 3 closed	Pins 2 & 3 closed
16-bit ISA	Pins 1 & 2 closed	Pins 1 & 2 closed