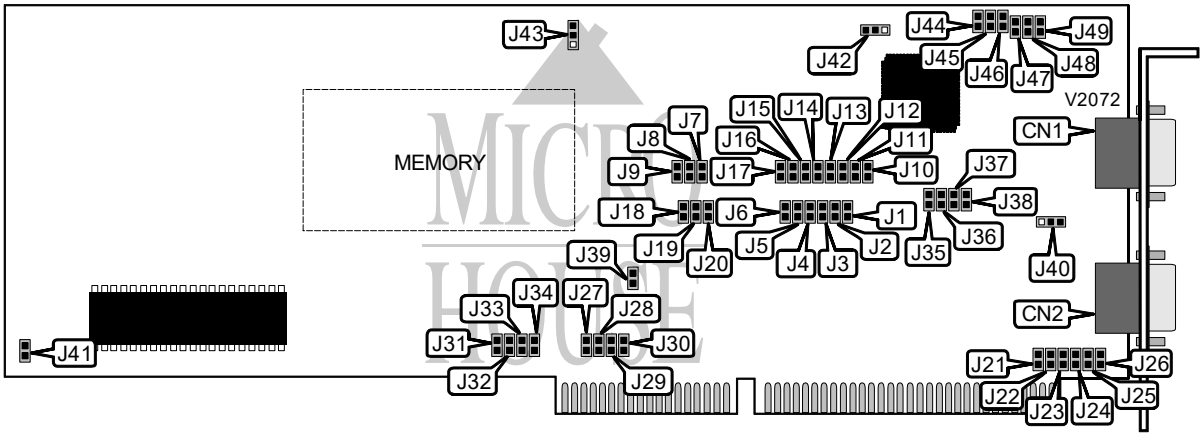
IMAGRAPH CORPORATION
PORTRAIT PLUS (051700)

Card Type	Video card
Video Chip Set	Brooktree BT548/Hitachi HD-63484
Highest Resolution Supported	1280 x 1024
Maximum Video Memory	2.5MB VRAM
I/O Options	VGA signal in, VGA signal out
Data Bus	8/16-bit ISA
Video Types Supported	XVGA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Video out to monitor	CN1	Video in from VGA card (passed through to CN1)	CN2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Card uses 8-bit data transfers to Hitachi ACRTC chip	J18	Closed
Card uses 16-bit data transfers to Hitachi ACRTC chip	J18	Open
í Card uses 8-bit data transfers to on-board memory	J19	Closed
Card uses 16-bit data transfers to on-board memory	J19	Open
í Card is installed in standard ISA slot	J20	Open
Card is installed in RT slot	J20	Closed
í Hitachi ACRTC chip enabled	J39	Open
Hitachi ACRTC chip disabled	J39	Closed
í Composite sync enabled on incoming VGA signal	J40	Pins 2 & 3 closed
Horizontal sync enabled on incoming VGA signal	J40	Pins 1 & 2 closed
í VGA pass-through disabled	J41	Open
VGA pass-through controlled by software	J41	Closed
í Composite sync enabled on outgoing VGA signal	J42	Pins 1 & 2 closed
Horizontal sync enabled on outgoing VGA signal	J42	Pins 2 & 3 closed
í Factory configured - do not alter	J43	Pins 1 & 2 closed

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INTERRUPT					
Setting	**J21**	**J22**	**J23**	**J24**	**J25**
IRQ3	Closed	Open	Open	Open	Open
IRQ4	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Closed
IRQ10	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open
í Disabled	Open	Open	Open	Open	Open

INTERRUPT (CON'T)					
Setting	**J26**	**J27**	**J28**	**J29**	**J30**
IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Closed	Open	Open	Open	Open
IRQ11	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Closed
í Disabled	Open	Open	Open	Open	Open

DMA CHANNEL								
Setting	**J31**	**J32**	**J33**	**J34**	**J35**	**J36**	**J37**	**J38**
DMA3	Closed	Open	Open	Open	Closed	Open	Open	Open
DMA5	Open	Closed	Open	Open	Open	Closed	Open	Open
DMA6	Open	Open	Closed	Open	Open	Open	Closed	Open
DMA7	Open	Open	Open	Closed	Open	Open	Open	Closed
í Disabled	Open	Open	Open	Open	Open	Open	Open	Open

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BASE I/O ADDRESS						
Setting	**J1**	**J2**	**J3**	**J4**	**J5**	**J6**
200h	Closed	Closed	Closed	Closed	Closed	Open
210h	Open	Closed	Closed	Closed	Closed	Open
220h	Closed	Open	Closed	Closed	Closed	Open
230h	Open	Open	Closed	Closed	Closed	Open
240h	Closed	Closed	Open	Closed	Closed	Open
í 2B0h	Open	Open	Closed	Open	Closed	Open
3B0h	Open	Open	Closed	Open	Open	Open
3C0h	Closed	Closed	Open	Open	Open	Open
3D0h	Open	Closed	Open	Open	Open	Open
3E0h	Closed	Open	Open	Open	Open	Open
3F0h	Open	Open	Open	Open	Open	Open

Note: A total of 32 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. J6 is the Most Significant Bit and jumper J1 is the Least Significant Bit. The jumpers have the following decimal values: J6=512, J5=256, J4=128, J3=64, J2=32, J1=16. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0)

PAGE SIZE			
Setting	**J7**	**J8**	**J9**
í 64KB	Closed	Closed	Closed
128KB	Open	Closed	Closed
256KB	Closed	Open	Closed
512KB	Open	Open	Closed
1MB	Closed	Closed	Open
2MB	Open	Open	Open

Note: When installing the board in an 8-bit slot the 64KB page size must be used.

SHARED RAM ADDRESS (64KB PAGE SIZE, 8-BIT ISA INSTALLATION)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
00000h	Closed	Closed	Closed	Closed	N/A	N/A	N/A	N/A
01000h	Open	Closed	Closed	Closed	N/A	N/A	N/A	N/A
02000h	Closed	Open	Closed	Closed	N/A	N/A	N/A	N/A
03000h	Open	Open	Closed	Closed	N/A	N/A	N/A	N/A
04000h	Closed	Closed	Open	Closed	N/A	N/A	N/A	N/A
05000h	Open	Closed	Open	Closed	N/A	N/A	N/A	N/A
06000h	Closed	Open	Open	Closed	N/A	N/A	N/A	N/A
07000h	Open	Open	Open	Closed	N/A	N/A	N/A	N/A
08000h	Closed	Closed	Closed	Open	N/A	N/A	N/A	N/A
09000h	Open	Closed	Closed	Open	N/A	N/A	N/A	N/A
0A000h	Closed	Open	Closed	Open	N/A	N/A	N/A	N/A
0B000h	Open	Open	Closed	Open	N/A	N/A	N/A	N/A
0C000h	Closed	Closed	Open	Open	N/A	N/A	N/A	N/A
í 0D000h	Open	Closed	Open	Open	N/A	N/A	N/A	N/A
0E000h	Closed	Open	Open	Open	N/A	N/A	N/A	N/A
0F000h	Open	Open	Open	Open	N/A	N/A	N/A	N/A

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SHARED RAM ADDRESS (64KB PAGE SIZE, 16-BIT ISA INSTALLATION)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
000000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
010000h	Open	Closed	Closed	Closed	Closed	Closed	Closed	Closed
020000h	Closed	Open	Closed	Closed	Closed	Closed	Closed	Closed
030000h	Open	Open	Closed	Closed	Closed	Closed	Closed	Closed
040000h	Closed	Closed	Open	Closed	Closed	Closed	Closed	Closed
FB0000h	Open	Open	Closed	Open	Open	Open	Open	Open
FC0000h	Closed	Closed	Open	Open	Open	Open	Open	Open
FD0000h	Open	Closed	Open	Open	Open	Open	Open	Open
FE0000h	Closed	Open	Open	Open	Open	Open	Open	Open
FF0000h	Open	Open	Open	Open	Open	Open	Open	Open

Note: A total of 256 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. J17 is the Most Significant Bit and jumper J10 is the Least Significant Bit. The jumpers have the following decimal values: J17= 8,388,608, J16=4,194,304, J15=2,097,152, J14=1,048,576, J13=524,288, J12=262,144, J11=131,072, J10=65,536. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0)

SHARED RAM ADDRESS (128KB PAGE SIZE)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
000000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
020000h	Closed	Open	Closed	Closed	Closed	Closed	Closed	Closed
040000h	Closed	Closed	Open	Closed	Closed	Closed	Closed	Closed
060000h	Closed	Open	Open	Closed	Closed	Closed	Closed	Closed
080000h	Closed	Closed	Closed	Open	Closed	Closed	Closed	Closed
F60000h	Closed	Open	Open	Closed	Open	Open	Open	Open
F80000h	Closed	Closed	Closed	Open	Open	Open	Open	Open
FA0000h	Closed	Open	Closed	Open	Open	Open	Open	Open
FC0000h	Closed	Closed	Open	Open	Open	Open	Open	Open
FE0000h	Closed	Open	Open	Open	Open	Open	Open	Open

Note: A total of 128 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. J17 is the Most Significant Bit and jumper J11 is the Least Significant Bit. The jumpers have the following decimal values: J17= 8,388,608, J16=4,194,304, J15=2,097,152, J14=1,048,576, J13=524,288, J12=262,144, J11=131,072. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0) Jumper J10 must always remain closed.

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SHARED RAM ADDRESS (256KB PAGE SIZE)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
000000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
040000h	Closed	Closed	Open	Closed	Closed	Closed	Closed	Closed
080000h	Closed	Closed	Closed	Open	Closed	Closed	Closed	Closed
0C0000h	Closed	Closed	Open	Open	Closed	Closed	Closed	Closed
100000h	Closed	Closed	Closed	Closed	Open	Closed	Closed	Closed
EC0000h	Closed	Closed	Open	Open	Closed	Open	Open	Open
F00000h	Closed	Closed	Closed	Closed	Open	Open	Open	Open
F40000h	Closed	Closed	Open	Closed	Open	Open	Open	Open
F80000h	Closed	Closed	Closed	Open	Open	Open	Open	Open
FC0000h	Closed	Closed	Open	Open	Open	Open	Open	Open

Note: A total of 64 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. J17 is the Most Significant Bit and jumper J12 is the Least Significant Bit. The jumpers have the following decimal values: J17= 8,388,608, J16=4,194,304, J15=2,097,152, J14=1,048,576, J13=524,288, J12=262,144. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0) Jumpers J10 and J11 must always remain closed.

SHARED RAM ADDRESS (512KB PAGE SIZE)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
000000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
080000h	Closed	Closed	Closed	Open	Closed	Closed	Closed	Closed
100000h	Closed	Closed	Closed	Closed	Open	Closed	Closed	Closed
180000h	Closed	Closed	Closed	Open	Open	Closed	Closed	Closed
200000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
D80000h	Closed	Closed	Closed	Open	Open	Closed	Open	Open
E00000h	Closed	Closed	Closed	Closed	Closed	Open	Open	Open
E80000h	Closed	Closed	Closed	Open	Closed	Open	Open	Open
F00000h	Closed	Closed	Closed	Closed	Open	Open	Open	Open
F80000h	Closed	Closed	Closed	Open	Open	Open	Open	Open

Note: A total of 32 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. J17 is the Most Significant Bit and jumper J13 is the Least Significant Bit. The jumpers have the following decimal values: J17= 8,388,608, J16=4,194,304, J15=2,097,152, J14=1,048,576, J13=524,288. Open the jumpers and add the values of the jumpers that are open to obtain the correct memory address. (Open=1, Closed=0) Jumpers J10, J11, and J12 must always remain closed.

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SHARED RAM ADDRESS (1MB PAGE SIZE)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
00000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
10000h	Closed	Closed	Closed	Closed	Open	Closed	Closed	Closed
20000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
30000h	Closed	Closed	Closed	Closed	Open	Open	Closed	Closed
40000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Closed
50000h	Closed	Closed	Closed	Closed	Open	Closed	Open	Closed
60000h	Closed	Closed	Closed	Closed	Closed	Open	Open	Closed
70000h	Closed	Closed	Closed	Closed	Open	Open	Open	Closed
80000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Open
90000h	Closed	Closed	Closed	Closed	Open	Closed	Closed	Open
A0000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Open
B0000h	Closed	Closed	Closed	Closed	Open	Open	Closed	Open
C0000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Open
D0000h	Closed	Closed	Closed	Closed	Open	Closed	Open	Open
E0000h	Closed	Closed	Closed	Closed	Closed	Open	Open	Open
F0000h	Closed	Closed	Closed	Closed	Open	Open	Open	Open

SHARED RAM ADDRESS (2MB PAGE SIZE)								
Setting	**J10**	**J11**	**J12**	**J13**	**J14**	**J15**	**J16**	**J17**
00000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
20000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
40000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Closed
60000h	Closed	Closed	Closed	Closed	Closed	Open	Open	Closed
80000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Open
A0000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Open
C0000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Open
E0000h	Closed	Closed	Closed	Closed	Closed	Open	Open	Open

CRYSTAL FREQUENCY DIVISOR						
Frequency	**J44**	**J45**	**J46**	**J47**	**J48**	**J49**
1/16	Closed	Open	Open	Closed	Open	Open
1/8	Open	Closed	Open	Open	Closed	Open
1/4	Open	Open	Closed	Open	Open	Closed