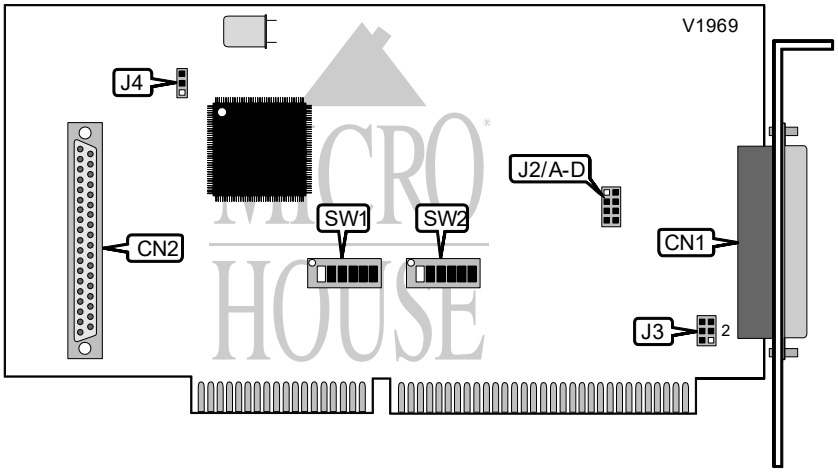
QUATECH, INC.
DAQ-801, DAQ-802

Card Type	Data Acquisition
Chip Set	Unidentified
I/O Options	Digital I/O
Data Bus	16-bit ISA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Main I/O	CN1	Auxiliary I/O	CN2

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
D/A converter 0 uses internal reference voltage	J3	Pins 4 & 5 closed
D/A converter 0 uses external reference voltage	J3	Pins 5 & 6 closed
D/A converter 1 uses internal reference voltage	J3	Pins 1 & 2 closed
D/A converter 1 uses external reference voltage	J3	Pins 2 & 3 closed
Internal clock source	J4	Pins 1 & 2 closed
External clock source	J4	Pins 2 & 3 closed

BASE I/O ADDRESS						
Setting	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
0000h	On	On	On	On	On	On
0010h	On	On	On	On	On	On
0020h	On	On	On	On	On	On
0030h	On	On	On	On	On	On
0040h	On	On	On	On	On	On
0210h	On	On	On	On	On	On
0280h	On	On	On	On	On	On
í 0300h	On	On	On	On	On	On
FFB0h	Off	Off	Off	Off	Off	Off
FFC0h	Off	Off	Off	Off	Off	Off
FFD0h	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off

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BASE I/O ADDRESS (CON'T)						
Setting	**SW2/1**	**SW2/2**	**SW2/3**	**SW2/4**	**SW2/5**	**SW2/6**
0000h	On	On	On	On	On	On
0010h	On	On	On	On	On	Off
0020h	On	On	On	On	Off	On
0030h	On	On	On	On	Off	Off
0040h	On	On	On	Off	On	On
0210h	Off	On	On	On	On	Off
0280h	Off	On	Off	On	On	On
í 0300h	Off	Off	On	On	On	On
FFB0h	Off	Off	Off	On	Off	Off
FFC0h	Off	Off	Off	Off	On	On
FFD0h	Off	Off	Off	Off	On	Off
FFE0h	Off	Off	Off	Off	Off	On
FFF0h	Off	Off	Off	Off	Off	Off

Note: A total of 4095 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW2/6 is the Least Significant Bit. The switches have the following decimal values: SW1/1=131072, SW1/2=65536, SW1/3=32768, SW1/4=16384, SW1/5=8192, SW1/6=4096, SW1/7=2048, SW1/8=1024, SW2/1=512, SW2/2=256, SW2/3=128, SW2/4=64, SW2/5=32, SW2/6=16. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

D/A CONVERTER 1 OUTPUT VOLTAGE		
Setting	**J2/A**	**J2/B**
0V to +5V	Open	Closed
-5V to +5V	Closed	Closed
0V to +10V	Open	Open
-10V to +10V	Closed	Open

D/A CONVERTER 2 OUTPUT VOLTAGE		
Setting	**J2/C**	**J2/D**
0V to +5V	Open	Closed
-5V to +5V	Closed	Closed
0V to +10V	Open	Open
-10V to +10V	Closed	Open