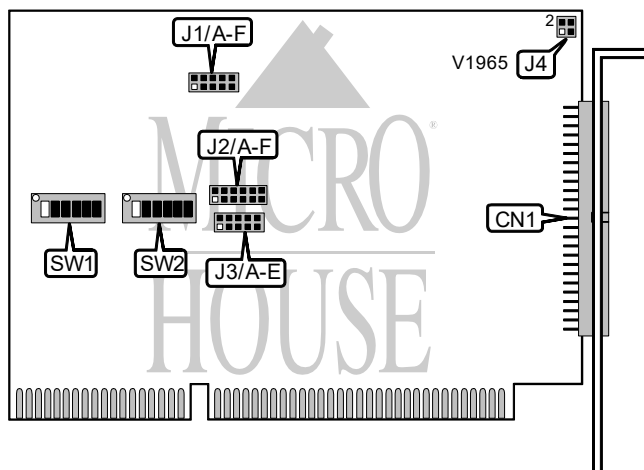


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P I O - 2 4 1

Card Type
Chip Set
I/O Options
Data Bus

Data Acquisition
Unidentified
PAMUX data bus
16-bit ISA



CONNECTIONS	
Function	Label
PAMUX data bus	CN1

USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í Factory configured - do not alter	J1/A	Open
í Factory configured - do not alter	J1/C	Open

BASE I/O ADDRESS						
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
0000h	On	On	On	On	On	On
0010h	On	On	On	On	On	On
0020h	On	On	On	On	On	On
0030h	On	On	On	On	On	On
0040h	On	On	On	On	On	On
í 0210h	On	On	On	On	On	On
0280h	On	On	On	On	On	On
0300h	On	On	On	On	On	On
FFB0h	Off	Off	Off	Off	Off	Off
FFC0h	Off	Off	Off	Off	Off	Off
FFD0h	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off

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BASE I/O ADDRESS (CON'T)						
Setting	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6
0000h	On	On	On	On	On	On
0010h	On	On	On	On	On	Off
0020h	On	On	On	On	Off	On
0030h	On	On	On	On	Off	Off
0040h	On	On	On	Off	On	On
0210h	Off	On	On	On	On	Off
0280h	Off	On	Off	On	On	On
0300h	Off	Off	On	On	On	On
FFB0h	Off	Off	Off	On	Off	Off
FFC0h	Off	Off	Off	Off	On	On
FFD0h	Off	Off	Off	Off	On	Off
FFE0h	Off	Off	Off	Off	Off	On
FFF0h	Off	Off	Off	Off	Off	Off
Note: A total of 4095 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW2/6 is the Least Significant Bit. The switches have the following decimal values: SW1/1=131072, SW1/2=65536, SW1/3=32768, SW1/4=16384, SW1/5=8192, SW1/6=4096, SW1/7=2048, SW1/8=1024, SW2/1=512, SW2/2=256, SW2/3=128, SW2/4=64, SW2/5=32, SW2/6=16. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)						

INTERRUPT						
Setting	J2/A	J2/B	J2/C	J2/D	J2/E	J2/F
IRQ2/9	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed
IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open

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INTERRUPT (CON'T)					
Setting	J3/A	J3/B	J3/C	J3/D	J3/E
IRQ2/9	Open	Open	Open	Open	Open
IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Closed	Open	Open	Open	Open
IRQ11	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Closed

INTERRUPT SOURCE				
Setting	J1/B	J1/D	J1/E	J4
í Internal source, pin 49 +5V DC	Open	Closed	Open	Pins 1 & 2 closed
External source on pin 49	Open	Open	Closed	Open
External source on pin 49 w/8254 counter	Closed	Open	Open	Pins 3 & 4 closed