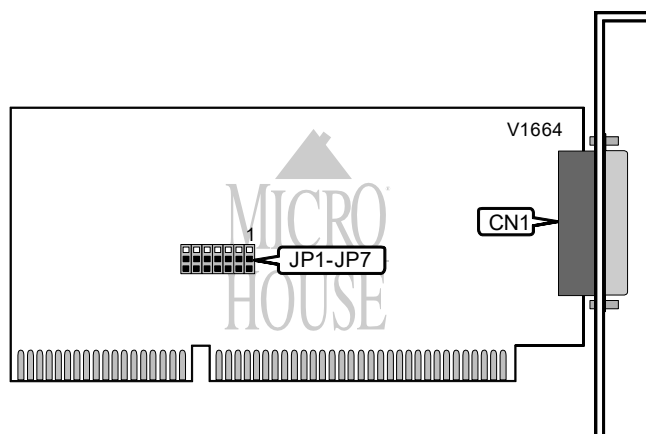


# SIIG, INC. PARALLEL PRO

Card Type  
Chip Set  
I/O Options  
Data Bus

Parallel card  
Unidentified  
parallel port  
16-bit ISA



CONNECTIONS	
Function	Label
Parallel port	CN1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Parallel pro enabled	JP7	Pins 1 & 2 closed
Parallel pro disabled	JP7	Pins 2 & 3 closed

MODE CONFIGURATION		
Mode	JP1	JP2
SPP/BPP	Pins 1 & 2 closed	Pins 1 & 2 closed
EPP	Pins 1 & 2 closed	Pins 2 & 3 closed
ECP	Pins 2 & 3 closed	Pins 1 & 2 closed
EPP/ECP	Pins 2 & 3 closed	Pins 2 & 3 closed

DMA SELECTION		
Setting	JP3	JP5
DMA 1	Pins 1 & 2 closed	Pins 1 & 2 closed
DMA 3	Pins 2 & 3 closed	Pins 2 & 3 closed

BASE I/O ADDRESS SELECTION	
Address	JP6
278h	Pins 1 & 2 closed
378h	Pins 2 & 3 closed

PARALLEL PORT INTERRUPT SELECTION	
IRQ	JP4
5	Pins 1 & 2 closed
7	Pins 2 & 3 closed