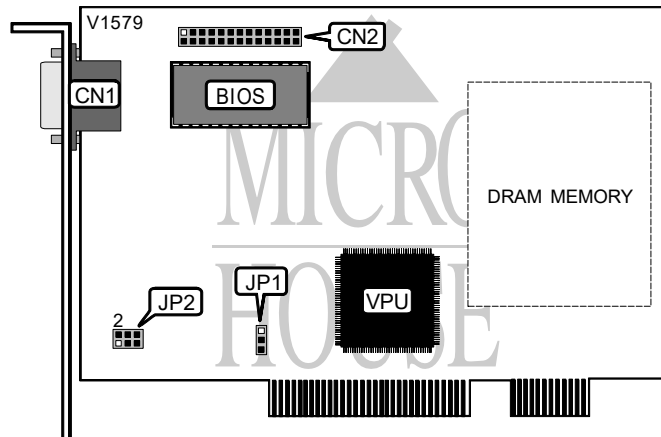


CARDEXPERT TECHNOLOGY, INC.

CIRRUS GD54M30/40 (VER. 1.0)

Category	Video
Video Types Supported	XVGA
Video Processor	Cirrus Logic GD54M30/40
Highest Resolution Supported	1280 x 1024
Data Bus Type	32-bit PCI
Memory Type	DRAM
Maximum Onboard Memory	2MB



CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	CN1	VGA feature connector	CN2

INTERRUPT CONFIGURATION	
IRQ 2/9	JP1
Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

DDC CONTROL LINE CONFIGURATION	
Setting	JP2
Disabled	Pins 3 & 5, 4 & 6 closed
Enabled	Pins 1 & 3, 2 & 4 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 8	NONE
2MB	(4) 256K x 8	(4) 256K x 8
Note: DRAM chip size can also be 256K x 16 depending on the Cirrus GD54M30/40 version you have. Relative location of Bank 0 & Bank 1 is unidentified by the manufacturer.		