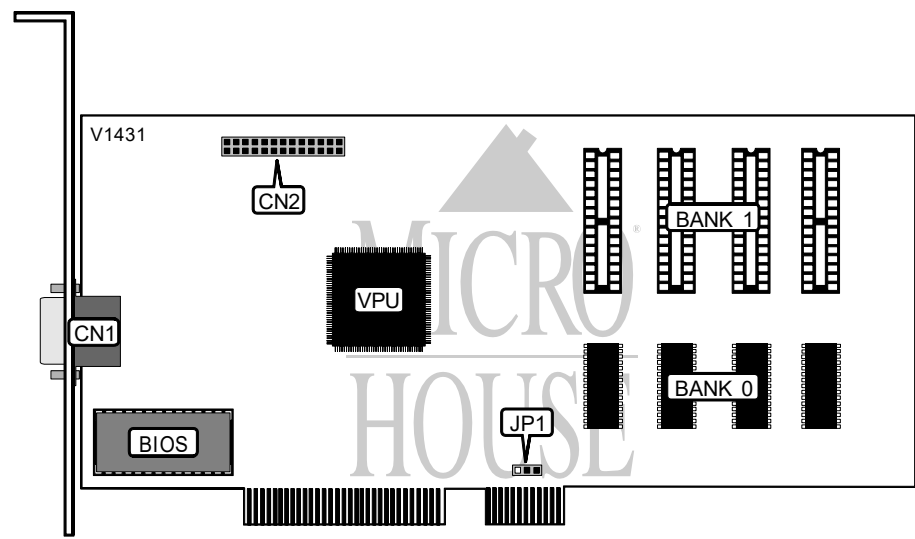


CHAINTECH COMPUTER COMPANY, LTD.  
GP-2000/GP-2000V

|                              |                  |
|------------------------------|------------------|
| Category                     | Video            |
| Video Types Supported        | XVGA             |
| Video Processor              | Ark Logic 2000PV |
| Highest Resolution Supported | 1024 x 768       |
| Data Bus Type                | 32-bit PCI       |
| Memory Type                  | DRAM             |
| Maximum Onboard Memory       | 2MB              |



| CONNECTIONS              |          |                       |          |
|--------------------------|----------|-----------------------|----------|
| Purpose :                | Location | Purpose :             | Location |
| 15-pin analog video port | CN1      | VGA feature connector | CN2      |

| LATENCY TIMER CONFIGURATION                                                                        |                   |
|----------------------------------------------------------------------------------------------------|-------------------|
| Setting                                                                                            | JP1               |
| Enabled                                                                                            | Pins 2 & 3 closed |
| Disabled                                                                                           | Pins 1 & 2 closed |
| Note: The disable setting only applies when using UMC 8886, 8891, & 8892 chipsets based mainboard. |                   |

| DRAM CONFIGURATION |              |              |
|--------------------|--------------|--------------|
| Memor              | Bank 1       | Bank 0       |
| 1MB                | (4) 256K x 8 | NONE         |
| 2MB                | (4) 256K x 8 | (4) 256K x 8 |