

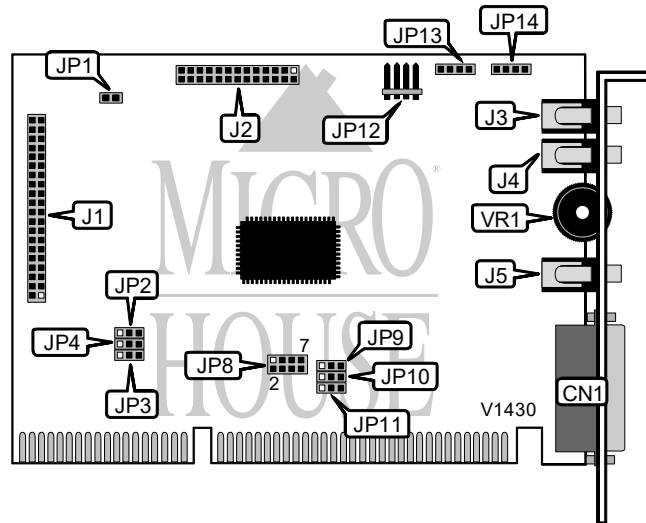
CHAINTECH COMPUTER COMPANY, LTD.

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Card Type
Chipset Controller
I/O Options

Maximum DRAM

Sound card
 Unidentified
 Audio connector (3), CD-ROM IDE interface, game/MIDI port, line in,
 microphone in, line out, volume, wavetable interface
 N/A



CONNECTIONS			
Purpose	Location	Purpose	Location
Game/MIDI port	CN1	LED connector	JP1
CD-ROM IDE interface	J1	Audio connector	JP12
Wavetable interface	J2	Audio connector	JP13
Line in	J3	Audio connector	JP14
Mic in	J4	Volume	VR1
Line out	J5		

BASE I/O ADDRESS SELECTION			
Address	JP10		JP11
220h	Pins 2 & 3 closed		Pins 2 & 3 closed
230h	Pins 2 & 3 closed		Pins 1 & 2 closed
240h	Pins 1 & 2 closed		Pins 2 & 3 closed
250h	Pins 1 & 2 closed		Pins 1 & 2 closed

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INTERRUPT SELECTION		
IRQ	JP8/Pins 5 & 6	JP8/Pins 7 & 8
i 7	Closed	Open
2/9	Open	Open
5	Open	Closed
10	Closed	Closed

DMA CHANNEL SELECTION		
Channel	JP8/Pins 1 & 2	JP8/Pins 3 & 4
i 1	Closed	Open
0	Open	Closed
3	Closed	Closed

IDE INTERRUPT CONFIGURATION		
IRQ	JP2	JP4
i 15 (Secondary)	Pins 1 & 2 closed	Pins 1 & 2 closed
14 (Primary)	Pins 2 & 3 closed	Pins 2 & 3 closed

IDE CONFIGURATION	
Setting	JP3
i Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

GAME PORT CONFIGURATION	
Setting	JP9
i Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed