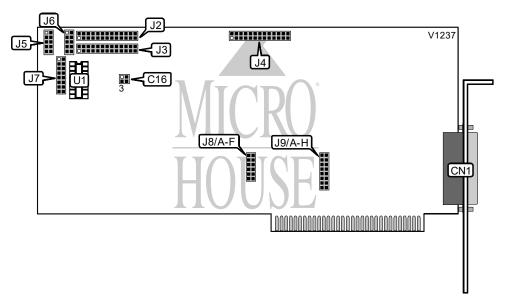
AST RESEARCH, INC. I/O PLUS II

Card Type Chipset Controller I/O Options Multi I/O card Unidentified

Apple game connector (2), IBM game connector, parallel port, serial port (2) $\ensuremath{\text{N/A}}$

Maximum DRAM



CONNECTIONS			
Purpos ;	Location	Purpos :	Location
Serial port 1 (DTE)	CN1	Apple II game connector	J5
Serial port 2 (DTE)	J2	Apple II game connector	J6
Serial port 2 (DCE)	J3	IBM game connector	J7
Parallel port	J4		

SERIAL 1 PORT CONFIGURATION					
COM	IRQ	J8/A	J8/B	J9/D	J9/E
COM 1	4	Closed	Open	Closed	Open
COM 2	3	Open	Closed	Open	Closed
Disabled	N/A	Open	Open	Open	Open

SERIAL PORT 2 CONFIGURATION			
Setting	IRQ	J8/C	J9/F
Enabled	3S	Closed	Closed
Disabled	N/A	Open	Open

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SERIAL PORT 2 DTE/DCE CONFIGURATION			
Setting	C16		
DCE	Pins 3 & 4 closed		
DTE	Pins 1 & 2 closed		

PARALLEL PORT CONFIGURATION				
Port	IRQ	J8/D	J8/E	J9/A
1 (LPT1)	7	Open	Closed	Closed
2 (LPT2)	7	Closed	Open	Closed
Disabled	N/A	Open	Open	Open

GAME PORT CONFIGURATION			
Setting J8/F			
Enabled	Closed		
Disabled	Open		
Note: There are two types of game ports on this card, an IBM & Apple type. When enabling the Apple game port			
(2) you must install a chip (74LS05) at U1. This chip must be removed when enabling the IBM game port.			

FACTORY CONFIGURED - DO NOT ALTER			
Jumper	Position		
J9/B	N/A		
J9/C	N/A		
J9/G	N/A		
J9/H	N/A		