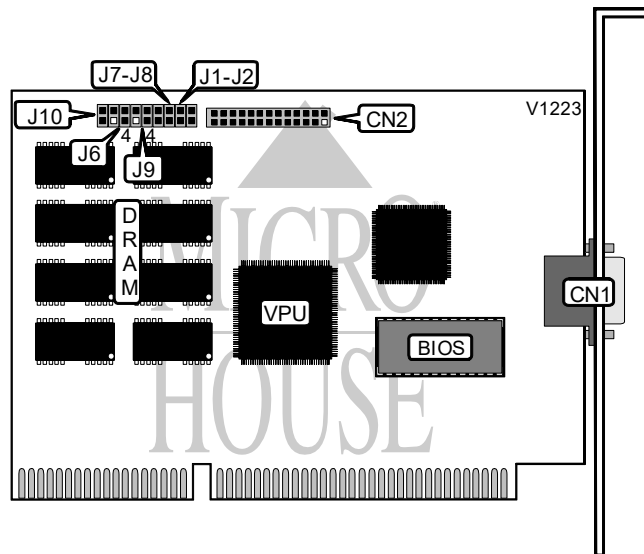


SIIG, INC.
SUPER VGA MASTER ISA

| | |
|------------------------------|------------|
| Category | Video |
| Video Types Supported | XVGA |
| Video Processor | Siig, Inc. |
| Highest Resolution Supported | 1024 x 768 |
| Data Bus Type | 16-bit ISA |
| Memory Type | DRAM |
| Maximum Onboard Memory | 1MB |



| CONNECTIONS | | | |
|--------------------------|----------|-----------------------|----------|
| Purpose | Location | Purpose | Location |
| 15-pin analog video port | CN1 | VGA feature connector | CN2 |

| FIRST STAGE ZERO-WAIT STATE CONFIGURATION | |
|---|--------|
| Setting | J2 |
| Enabled | Closed |
| Disabled | Open |

| SECOND STAGE ZERO-WAIT STATE CONFIGURATION | |
|--|--------|
| Setting | J1 |
| Enabled | Open |
| Disabled | Closed |

Continued on next page . . .

SIIG, INC.
SUPER VGA MASTER ISA

...continued from previous page

| SCAN RATE CONFIGURATION | |
|-------------------------|--------|
| Rate | J7 |
| í <48.7 KHz | Closed |
| ≥48.7 KHz | Open |

| BIOS AUTODETECT CONFIGURATION | |
|-------------------------------|--------|
| Setting | J8 |
| í Enabled | Closed |
| Disabled | Open |

| ISA BUS SLOT SELECTION | | | |
|------------------------|--------------------------|--------------------------|--------|
| Bus | J6 | J9 | 10 |
| í 16-bit | Open | Open | Closed |
| 8-bit | Pins 1 & 2, 3 & 4 closed | Pins 1 & 2, 3 & 4 closed | Open |