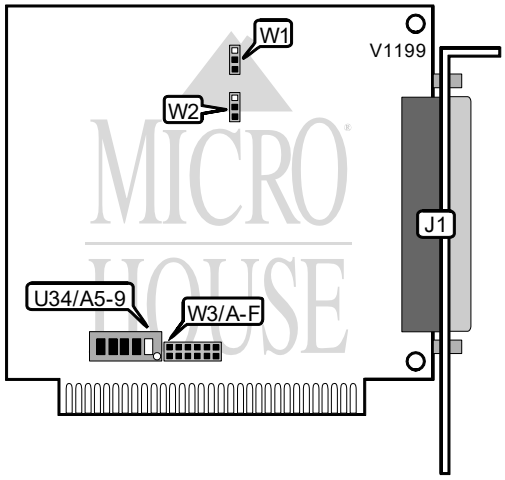
NATIONAL INSTRUMENTS
P C - L P M - 1 6

Card Type Analog to digital timing converter
Chipset Controller Unidentified
I/O Options Analog input, digital input, digital output
Maximum DRAM N/A



CONNECTIONS	
Purpose	Location
50-pin I/O connector	J1

INTERRUPT SELECTION						
IRQ	W3/A	W3/B	W3/C	W3/D	W3/E	W3/F
í 5	Open	Open	Closed	Open	Open	Open
3	Closed	Open	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open	Open
6	Open	Open	Open	Closed	Open	Open
7	Open	Open	Open	Open	Closed	Open
9	Open	Open	Open	Open	Open	Closed

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BASE I/O ADDRESS SELECTION					
Address	U34/A5	U34/A6	U34/A7	U34/A8	U34/A9
í 260h	Off	Off	On	On	Off
000h	On	On	On	On	On
020h	Off	On	On	On	On
040h	On	Off	On	On	On
060h	Off	Off	On	On	On
080h	On	On	Off	On	On
360h	Off	Off	On	Off	Off
380h	On	On	Off	Off	Off
3A0h	Off	On	Off	Off	Off
3C0h	On	Off	Off	Off	Off
3E0h	Off	Off	Off	Off	Off

Note: A total of 255 base address settings are available. The switches are a binary representation of the decimal memory addresses. Switch A9 is the Most Significant Bit and switch A5 is the Least Significant Bit. The switches have the following decimal values: switch A9=512, A8=256, A7=128, A6=64, A5=32. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

BIPOLAR INPUT SELECTION		
Setting	W1	W2
±5V	Pins 2 & 3 closed	Pins 2 & 3 closed
±2.5V	Pins 2 & 3 closed	Pins 1 & 2 closed

UNIPOLAR INPUT SELECTION		
Setting	W1	W2
0 to 10V	Pins 1 & 2 closed	Pins 2 & 3 closed
0 to 5V	Pins 2 & 3 closed	Pins 2 & 3 closed