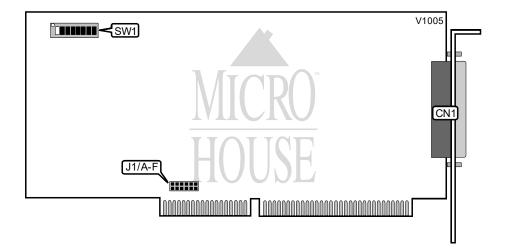
## MULTITECH SYSTEMS, INC. I S I 5 0 8 U

Card Type Chipset Controller I/O Options Maximum DRAM

Serial card N/A Serial ports (8 on external panel) N/A



CONNECTIONS					
Purpose	Location				
78-pin serial port connector	CN1				

I/O BASE ADDRESS SELECTION									
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8	
í 380h	On	On	On	On	Off	Off	Off	On	
100h	On	On	On	On	On	Off	On	On	
108h	Off	On	On	On	On	Off	On	On	
110h	On	Off	On	On	On	Off	On	On	
118h	Off	Off	On	On	On	Off	On	On	
120h	On	On	Off	On	On	Off	On	On	
3D8h	Off	Off	On	Off	Off	Off	Off	On	
3E0h	On	On	Off	Off	Off	Off	Off	On	
3E8h	Off	On	Off	Off	Off	Off	Off	On	
3F0h	On	Off	Off	Off	Off	Off	Off	On	
3F8h	Off	On							
<ul> <li>Note: A total of 96 base address settings are available. The switches are a binary representation of the decimal memory addresses. Switch SW1/8 is the Most Significant Bit and switch SW1/1 is the Least Significant Bit. The switches have the following decimal values: switch SW1/1=8, SW1/2=16, SW1/3=32, SW1/4=64, SW1/5=128, SW1/6=256, SW1/7=512, SW1/8=1024. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)</li> </ul>									

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	INTERRUPT SELECTION								
IRQ	J1/A	J1/B	J1/C	J1/D	J1/E	J1/F			
í 3	Open	Closed	Open	Open	Open	Open			
2/9	Closed	Open	Open	Open	Open	Open			
4	Open	Open	Closed	Open	Open	Open			
5	Open	Open	Open	Closed	Open	Open			
6	Open	Open	Open	Open	Closed	Open			
7	Open	Open	Open	Open	Open	Closed			