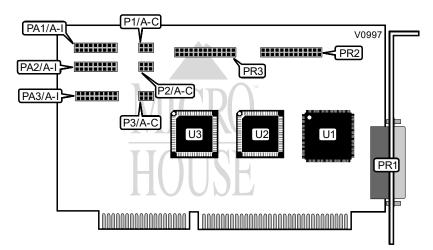
SIIG, INC. I/O EXPANDER 3P

Card Type Parallel card
Chipset Controller Siig, Inc.
I/O Options Parallel ports (3)

Maximum DRAM N/A



CONNECTIONS							
Purpos :	Location	Purpos :	Location				
25-pin parallel port	PR1	26-pin parallel port interface	PR3				
26-pin parallel port interface	PR2						

PARALLEL PORT 1 CONFIGURATION							
LPT	P1/A	P1/B	P1/C				
í LPT3 (3BCh-3BEh)	Closed	Open	Open				
LPT1 (378h-37Fh)	Open	Closed	Open				
LPT2 (278h-27Fh)	Open	Open	Closed				
Disabled	Open	Open	Open				

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	PARALLEL PORT 1 INTERRUPT SELECTION								
IRQ	PA1/A	PA1/B	PA1/C	PA1/D	PA1/E	PA1/F	PA1/G	PA1/H	PA1/I
í 7	Open	Open	Open	Closed	Open	Open	Open	Open	Open
2/9	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Closed	Open							
4	Open	Closed	Open						
5	Open	Open	Closed	Open	Open	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Open	Open	Open	Closed
Disabled	Open	Open	Open	Open	Open	Open	Open	Open	Open

PARALLEL PORT 2 CONFIGURATION							
LPT	P2/A	P2/B	P2/C				
í LPT1 (378h-37Fh)	Open	Closed	Open				
LPT2 (278h-27Fh)	Open	Open	Closed				
LPT3 (3BCh-3BEh)	Closed	Open	Open				
Disabled	Open	Open	Open				

	PARALLEL PORT 2 INTERRUPT SELECTION								
IRQ	PA2/A	PA2/B	PA2/C	PA2/D	PA2/E	PA2/F	PA2/G	PA2/H	PA2/I
í 5	Open	Open	Closed	Open	Open	Open	Open	Open	Open
2/9	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Closed	Open							
4	Open	Closed	Open						
7	Open	Open	Open	Closed	Open	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Open	Open	Open	Closed
Disabled	Open	Open	Open	Open	Open	Open	Open	Open	Open

PARALLEL PORT 3 CONFIGURATION							
LPT	P3/A	P3/B	P3/C				
í LPT2 (278h-27Fh)	Open	Open	Closed				
LPT1 (378h-37Fh)	Open	Closed	Open				
LPT3 (3BCh-3BEh)	Closed	Open	Open				
Disabled	Open	Open	Open				

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	PARALLEL PORT 3 INTERRUPT SELECTION								
IRQ	PA3/A	PA3/B	PA3/C	PA3/D	PA3/E	PA3/F	PA3/G	PA3/H	PA3/I
í 10	Open	Open	Open	Open	Open	Closed	Open	Open	Open
2/9	Open	Open	Open	Open	Closed	Open	Open	Open	Open
3	Closed	Open							
4	Open	Closed	Open						
5	Open	Open	Closed	Open	Open	Open	Open	Open	Open
7	Open	Open	Open	Closed	Open	Open	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Closed	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Closed	Open
15	Open	Open	Open	Open	Open	Open	Open	Open	Closed
Disabled	Open	Open	Open	Open	Open	Open	Open	Open	Open

PARALLEL PORT 2 & 3 ACTIVATION						
Chip Socket						
Controller U2						
Controller U3						
Note: To activate parallel ports 2 & 3 upgrade chips must be installed.						