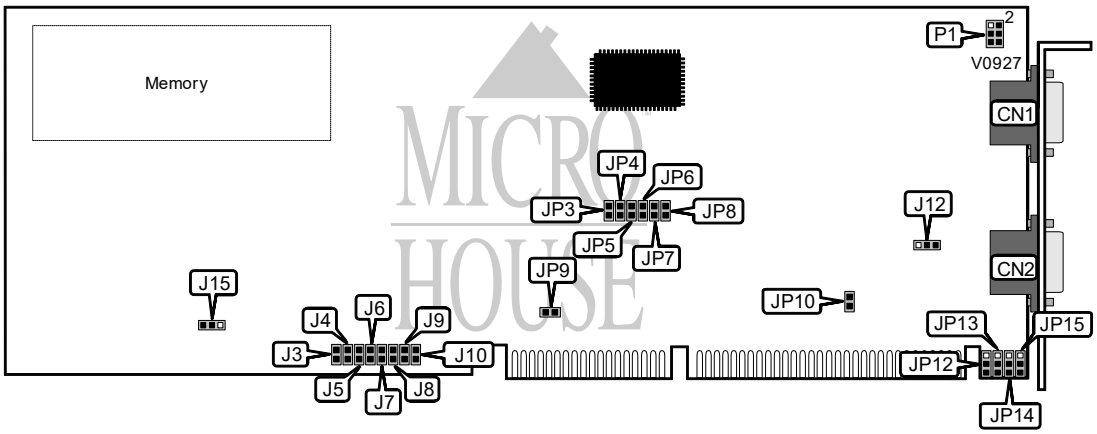
IMAGRAPH CORPORATION
TI-1210-8 (REV. C)

Category	Video
Video Types Supported	XVGA
Video Processor	Texas Instruments TMS34010
Highest Resolution Supported	1280 x 1024
Data Bus Type	16-bit ISA
Memory Type	Unidentified
Maximum Onboard Memory	Unidentified



CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	CN1	VGA pass-through connector	CN2

BASE I/O ADDRESS SELECTION								
Address	**J3**	**J4**	**J5**	**J6**	**J7**	**J8**	**J9**	**J10**
00000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Closed
01000h	Closed	Closed	Closed	Closed	Closed	Closed	Closed	Open
02000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Closed
03000h	Closed	Closed	Closed	Closed	Closed	Closed	Open	Open
04000h	Closed	Closed	Closed	Closed	Closed	Open	Closed	Closed
FB000h	Open	Open	Open	Open	Open	Closed	Open	Open
FC000h	Open	Open	Open	Open	Open	Open	Closed	Closed
FD000h	Open	Open	Open	Open	Open	Open	Closed	Open
FE000h	Open	Open	Open	Open	Open	Open	Open	Closed
FF000h	Open	Open	Open	Open	Open	Open	Open	Open

Note: A total of 255 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. Jumper J3 is the Most Significant Bit and jumper J10 is the Least Significant Bit. The jumpers have the following decimal values: jumper J3=524288, J4=262144, J5=131072, J6=65536, J7=32768, J8=16384, J9=8192, J10=4096. Open the jumpers and add the values of the open jumpers to obtain the correct memory address. (open=1, closed=0)

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EMULATION CONFIGURATION	
Setting	JP10
Enabled	Open
Disabled	Closed

VGA PASS-THROUGH CONFIGURATION	
Setting	P1/Pins 5 & 6
Enabled	Closed
Disabled	Open

VGA PASS-THROUGH SYNC SIGNAL CONFIGURATION	
Mode	J12
í Composite sync	Pins 1 & 2 closed
Horizontal sync	Pins 2 & 3 closed

SYNC SIGNAL CONFIGURATION	
Mode	J15
í Composite sync	Pins 2 & 3 closed
Horizontal sync	Pins 1 & 2 closed

TRANSFER WIDTH CONFIGURATION	
Setting	JP9
í Disabled	Open
Enabled	Closed

INTERRUPT SELECTION				
IRQ	**JP12**	**JP13**	**JP14**	**JP15**
í Disabled	Open	Open	Open	Open
2/9	Open	Pins 1 & 2 closed	Open	Open
6	Open	Pins 2 & 3 closed	Open	Open
7	Pins 2 & 3 closed	Open	Open	Open
10	Open	Open	Open	Pins 2 & 3 closed
11	Open	Open	Open	Pins 1 & 2 closed
12	Open	Open	Pins 1 & 2 closed	Open
14	Pins 1 & 2 closed	Open	Open	Open
15	Open	Open	Pins 2 & 3 closed	Open

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DIRECT ACCESS ADDRESS SELECTION						
Address	**JP3**	**JP4**	**JP5**	**JP6**	**JP7**	**JP8**
00000h	Closed	Closed	Closed	Closed	Closed	Closed
04000h	Closed	Closed	Closed	Closed	Closed	Open
08000h	Closed	Closed	Closed	Closed	Open	Closed
0C000h	Closed	Closed	Closed	Closed	Open	Open
10000h	Closed	Closed	Closed	Open	Closed	Closed
EC000h	Open	Open	Open	Closed	Open	Open
F0000h	Open	Open	Open	Open	Closed	Closed
F4000h	Open	Open	Open	Open	Closed	Open
F8000h	Open	Open	Open	Open	Open	Closed
FC000h	Open	Open	Open	Open	Open	Open

Note: A total of 63 base address settings are available. The jumpers are a binary representation of the decimal memory addresses. Jumper JP3 is the Most Significant Bit and jumper JP8 is the Least Significant Bit. The jumpers have the following decimal values: jumper JP3=524288, JP4=262144, JP5=131072, JP6=65536, JP7=32768, JP8=16384. Open the jumpers and add the values of the open jumpers to obtain the correct memory address. (open=1, closed=0)

FACTORY CONFIGURED - DO NOT ALTER	
Jumper	**Position**
JP1	Unidentified
JP2	Unidentified

Note: Exact location of jumpers unidentified.