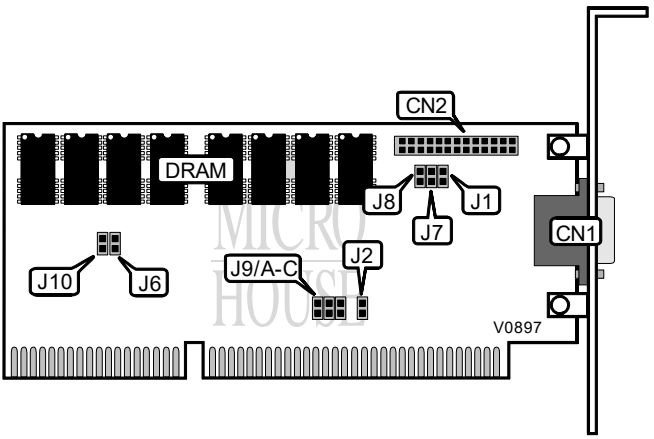


# JATON CORPORATION

## TVGA 8900CL

<b>Category</b>	Video
<b>Video Types Supported</b>	XVGA
<b>Video Processor</b>	Trident 8900
<b>Highest Resolution Supported</b>	1024 x 768
<b>Data Bus Type</b>	16-bit ISA
<b>Memory Type</b>	DRAM
<b>Maximum Onboard Memory</b>	1024KB



CONNECTIONS			
Purpose :	Location	Purpose :	Location
15-pin analog video port	CN1	VESA Feature connector	CN2

FIRST STAGE ZERO WAIT STATE CONFIGURATION	
Setting	J2
i Enabled	Closed
Disabled	Open

SECOND STAGE ZERO WAIT STATE CONFIGURATION	
Setting	J1
i Enabled	Open
Disabled	Closed

SCAN RATE SELECTION	
Setting	J7
i Normal	Closed
48.7KHz	Open

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JATON CORPORATION  
TVGA 8900CL

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BIOS AUTODETECT CONFIGURATION	
Setting	J8
Enabled	Open
Disabled	Closed

BUS TYPE SELECTION					
Type	J6	J9/A	J9/B	J9/C	J10
16-bit ISA	Open	Open	Open	Open	Closed
8-bit ISA	Closed	Closed	Closed	Closed	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(4) 256K x 4	NONE
1MB	(4) 256K x 4	(4) 256K x 4
Note: The relative locations of Bank 0 and Bank 1 are unidentified.		