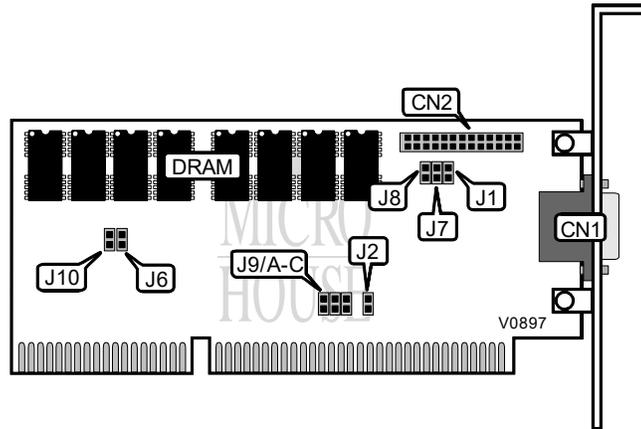


JATON CORPORATION
TVGA 8900CL

Category Video
 Video Types Supported XVGA
 Video Processor Trident 8900
 Highest Resolution Supported 1024 x 768
 Data Bus Type 16-bit ISA
 Memory Type DRAM
 Maximum Onboard Memory 1024KB



CONNECTIONS			
Purpose :	Location	Purpose :	Location
15-pin analog video port	CN1	VESA Feature connector	CN2

FIRST STAGE ZERO WAIT STATE CONFIGURATION	
Setting	J2
Enabled	Closed
Disabled	Open

SECOND STAGE ZERO WAIT STATE CONFIGURATION	
Setting	J1
Enabled	Open
Disabled	Closed

SCAN RATE SELECTION	
Setting	J7
Normal	Closed
48.7KHz	Open

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TVGA 8900CL

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BIOS AUTODETECT CONFIGURATION	
Setting	J8
Enabled	Open
Disabled	Closed

BUS TYPE SELECTION					
Type	J6	J9/A	J9/B	J9/C	J10
16-bit ISA	Open	Open	Open	Open	Closed
8-bit ISA	Closed	Closed	Closed	Closed	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(4) 256K x 4	NONE
1MB	(4) 256K x 4	(4) 256K x 4
Note: The relative locations of Bank 0 and Bank 1 are unidentified.		