QUATECH, INC.
ES100M/ES100MS/QS-100M/QS100MS

Category	Serial controller
Chipset Controller	Unidentified 16550s
I/O Options Supported	Serial ports (QS-100 = 4, ES100 = 8)
Maximum Onboard Memory	N/A
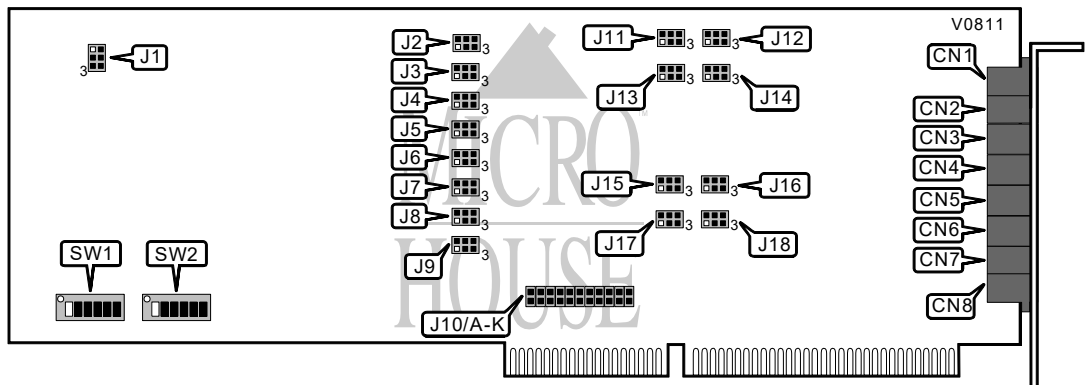
CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1 - RJ-11	CN1	Serial port 5 - RJ-11	CN5
Serial port 2 - RJ-11	CN2	Serial port 6 - RJ-11	CN6
Serial port 3 - RJ-11	CN3	Serial port 7 - RJ-11	CN7
Serial port 4 - RJ-11	CN4	Serial port 8 - RJ-11	CN8
Note: The QS-100 is equipped with 4 RJ-11 ports.			

I/O ADDRESS CONFIGURATION											
	SW1						SW2				
Base Address	1	2	3	4	5	6	1	2	3	4	5
0300h	On	On	On	On	On	On	Off	Off	On	On	On
5AC0h	On	Off	On	Off	Off	On	Off	On	Off	Off	On
Note: The address range for the QS-100M/ES-100M is from 0 to FFFFh. The switches are a binary representation of the addresses. When a switch is off, the corresponding bit is set to 1 and has the following decimal value: SW1/1=8, SW1/2=4, SW1/3=2, SW1/4=1, SW1/5=8, SW1/6=4, SW2/1=2, SW2/2=1, SW2/3=8, SW2/4=4, SW2/5=2. SW2/5 is not used on the ES-100M. The QS-100M requires 32 consecutive address locations, and the ES-100M requires 64 consecutive address locations.											

INPUT CLOCK FREQUENCY	
Frequency	J1
1.8432MHz	Pins 1 & 2, 4 & 5, 3 & 6 closed
18.432MHz	Pins 2 & 5 closed
9.216MHz	Pins 1 & 2, 5 & 6 closed
3.6864MHz	Pins 2 & 3, 4 & 5 closed

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INTERRUPT STATUS REGISTER	
Setting	SW1/6
í Disabled (scratchpad register enabled)	Off
Enabled	On

HANDSHAKE OPTIONS	
Setting	J(2 - 9)
RTS/CTS handshake, DTR/DSR loopback	Pins 1 & 2, 4 & 5, 3 & 6 closed
RTS/CTS loopback, DTR/DSR handshake	Pins 1 & 4, 2 & 3, 5 & 6 closed
RTS/CTS loopback, DTR/DSR loopback, AUXIN/AUXOUT loopback	Pins 1 & 4, 2 & 5, 3 & 6 closed
Note: J2 - J9 control the configurations of CN1 - CN8, respectively. Each RJ-11 port may be configured differently.	

INTERRUPT SELECT - J10											
IRQ	A	B	C	D	E	F	G	H	I	J	K
íIRQ3	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open
IRQ2	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed

CARRIER DETECT (DCD) SIGNAL INPUT	
Setting	J(11 - 18)
í RJ-11 connected to chassis ground (DCD input disabled)	Pins 1 & 2, 4 & 5 closed
DCD input enabled	Pins 2 & 3, 5 & 6 closed
Note: Pin 3 of the RJ-11 port must be connected to chassis ground or the DCD signal, depending on desired configuration. J11 - J18 control the configurations of CN1 - CN8, respectively. Each RJ-11 port may be configured differently.	