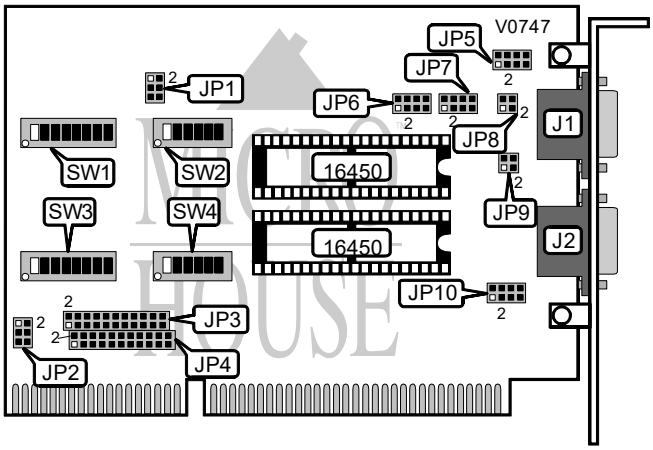
QUATECH, INC.
TV-200/TV-300

Card Type Serial interface
Chipset Controller Unidentified
I/O Options Serial ports (2)
Maximum Dram N/A



CONNECTIONS			
Purpos ›	Location	Purpos ›	Location
Serial port 1	J1	Serial port 2	J2

SERIAL PORT CONFIGURATION			
F ›rt 1	F ›rt 2	S V2/6	S V4/6
Enabled	Enabled	On	On
Enabled	Disabled	On	Off
Disabled	Enabled	Off	On
Disabled	Disabled	Off	Off

SERIAL INTERRUPT SHARING SELECTION		
Port 1	Port 2	JP2
Non-sharable	Non-sharable	Pins 1 & 2, 4 & 5 closed
Non-sharable	Sharable	Pins 1 & 2, 5 & 6 closed
Sharable	Non-sharable	Pins 2 & 3, 4 & 5 closed
Sharable	Sharable	Pins 2 & 3, 5 & 6 closed

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PORT 1 ADDRESS SELECT							
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	On
0010h	On	On	On	On	On	On	On
0018h	On	On	On	On	On	On	On
02E8h (COM4:)	On	On	On	On	On	On	Off
02F8h (COM2:)	On	On	On	On	On	On	Off
03E8h (COM3:)	On	On	On	On	On	On	Off
03F8h (COM1:)	On	On	On	On	On	On	Off
FFE0h	Off	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off	Off

PORT 1 ADDRESS SELECT (CON'T)						
Address	SW1/8	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	Off
0010h	On	On	On	On	Off	On
0018h	On	On	On	On	Off	Off
02E8h (COM4:)	On	Off	Off	Off	On	Off
02F8h (COM2:)	On	Off	Off	Off	Off	Off
03E8h (COM3:)	Off	Off	Off	Off	On	Off
03F8h (COM1:)	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off

Note: A total of 8191 memory base address settings are available. The switches are a binary representation of the decimal addresses. Switch 2/5 is the Least Significant Bit and switch 1/1 is the Most Significant Bit. The switches have the following decimal values: switch 2/5=1, 2/4=2, 2/3=4, 2/2=8, 2/1=16, 1/8=32, 1/7=64, 1/6=128, 1/5=256, 1/4=512, 1/3=1024, 1/2=2048, 1/1=4096. Turn off the switches and add the off switches to obtain the correct memory base address. (On=0, Off=1)

DUPLEX MODE SELECTION			
Port 1	Port 2	P6	P7
Full duplex	Full duplex	Pins 5 & 6 closed	Pins 5 & 6 closed
Full duplex	Half duplex	Pins 2 & 6 closed	Pins 5 & 6 closed
Half duplex	Full duplex	Pins 5 & 6 closed	Pins 2 & 6 closed
Half duplex	Half duplex	Pins 2 & 6 closed	Pins 2 & 6 closed

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PORT 2 ADDRESS SELECT							
Address	SW3/1	SW3/2	SW3/3	SW3/4	SW3/5	SW3/6	SW3/7
0000h	On	On	On	On	On	On	On
0008h	On	On	On	On	On	On	On
0010h	On	On	On	On	On	On	On
0018h	On	On	On	On	On	On	On
02E8h (COM4:)	On	On	On	On	On	On	Off
02F8h (COM2:)	On	On	On	On	On	On	Off
03E8h (COM3:)	On	On	On	On	On	On	Off
03F8h (COM1:)	On	On	On	On	On	On	Off
FFE0h	Off	Off	Off	Off	Off	Off	Off
FFE8h	Off	Off	Off	Off	Off	Off	Off
FFF0h	Off	Off	Off	Off	Off	Off	Off
FFF8h	Off	Off	Off	Off	Off	Off	Off

PORT 2 ADDRESS SELECT (CON'T)						
Address	SW3/8	SW4/1	SW4/2	SW4/3	SW4/4	SW4/5
0000h	On	On	On	On	On	On
0008h	On	On	On	On	On	Off
0010h	On	On	On	On	Off	On
0018h	On	On	On	On	Off	Off
02E8h (COM4:)	On	Off	Off	Off	On	Off
02F8h (COM2:)	On	Off	Off	Off	Off	Off
03E8h (COM3:)	Off	Off	Off	Off	On	Off
03F8h (COM1:)	Off	Off	Off	Off	Off	Off
FFE0h	Off	Off	Off	Off	On	On
FFE8h	Off	Off	Off	Off	On	Off
FFF0h	Off	Off	Off	Off	Off	On
FFF8h	Off	Off	Off	Off	Off	Off

Note: A total of 8191 memory base address settings are available. The switches are a binary representation of the decimal addresses. Switch 4/5 is the Least Significant Bit and switch 3/1 is the Most Significant Bit. The switches have the following decimal values: switch 4/5=1, 4/4=2, 4/3=4, 4/2=8, 4/1=16, 3/8=32, 3/7=64, 3/6=128, 3/5=256, 3/4=512, 3/3=1024, 3/2=2048, 3/1=4096. Turn off the switches and add the off switches to obtain the correct memory base address. (On=0, Off=1)

TERMINATOR RESISTOR CONFIGURATION			
Port 1	Port 2	P8	P9
Terminated	Terminated	Pins 1 & 3, 2 & 4 closed	Pins 1 & 3, 2 & 4 closed
Terminated	Not terminated	Pins 1 & 3, 2 & 4 closed	Open
Not terminated	Terminated	Open	Pins 1 & 3, 2 & 4 closed
Not terminated	Not terminated	Open	Open

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5

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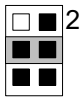
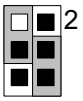
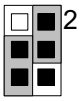
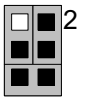
PORT 1 INTERRUPT SELECTION	
IRQ	**JP3**
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
6	Pins 9 & 10 closed
7	Pins 11 & 12 closed

PORT 2 INTERRUPT SELECTION	
IRQ	**JP4**
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
6	Pins 9 & 10 closed
7	Pins 11 & 12 closed

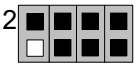
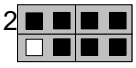
INPUT CLOCK DIVISOR SELECTION

JP1

Divide by 10 (Default)	Divide by 5	Divide by 2	Divide by 1
			

CONTROLLER/TRIBUTARY MODE SELECTION FOR PORT 1

JP5

Tributary	Controller
	

CONTROLLER/TRIBUTARY MODE SELECTION FOR PORT 2

JP10

Tributary	Controller