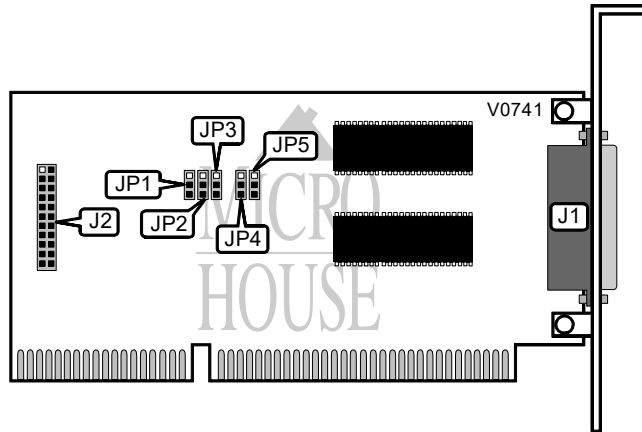


KOUWELL ELECTRONIC CORPORATION
KW-508-2

Card Type Parallel interface
Chipset Controller Unidentified
I/O Options Parallel ports (2)
Maximum Dram N/A



CONNECTIONS	
Purpose	Location
Parallel port 1	J1
Parallel port 2	J2

PORT CONFIGURATION				
Port 1	Port 2	JP1	JP2	JP3
LPT1	LPT2	Open	Pins 1 & 2 closed	Pins 2 & 3 closed
LPT1	LPT3	Pins 1 & 2 closed	Open	Pins 2 & 3 closed
LPT1	Disabled	Open	Open	Pins 2 & 3 closed
LPT2	LPT1	Open	Pins 2 & 3 closed	Pins 1 & 2 closed
LPT2	LPT3	Pins 1 & 2 closed	Pins 2 & 3 closed	Open
LPT2	Disabled	Open	Pins 2 & 3 closed	Open
LPT3	LPT1	Pins 2 & 3 closed	Open	Pins 1 & 2 closed
LPT3	LPT2	Pins 2 & 3 closed	Pins 1 & 2 closed	Open
LPT3	Disabled	Pins 2 & 3 closed	Open	Open
Disabled	LPT1	Open	Open	Pins 1 & 2 closed
Disabled	LPT2	Open	Pins 1 & 2 closed	Open
Disabled	LPT3	Pins 1 & 2 closed	Open	Open
Disabled	Disabled	Open	Open	Open

INTERRUPT SELECTION			
Port 1	Port 2	P4	P5
IRQ5	IRQ7	Pins 2 & 3 closed	Pins 1 & 2 closed
IRQ7	IRQ5	Pins 1 & 2 closed	Pins 2 & 3 closed