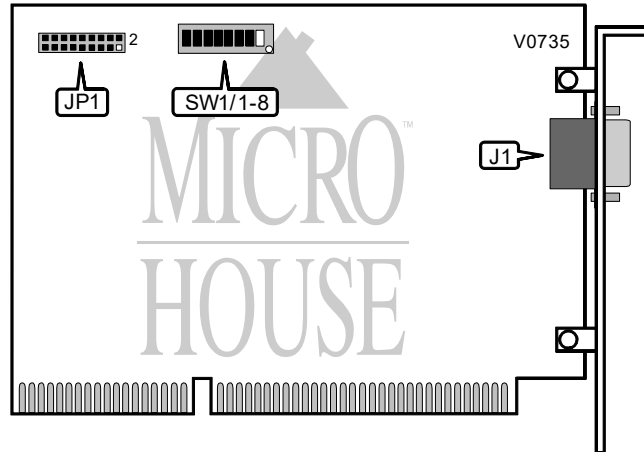


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ISI551PC

Card Type	Serial interface
Chip Set	Unidentified
Maximum Onboard Memory	Unidentified
I/O Options	Serial port
Data Bus	16-bit ISA (EISA also available)



CONNECTIONS	
Purpose	Location
Serial port 1	J1

USER CONFIGURABLE SETTINGS		
	Label	Location
í Factory configured do not alter	SW1/8	On

PORT 1 ADDRESS SELECTION					
IRQ	JP1/ Pins 1 & 2	JP1/ Pins 3 & 4	JP1/ Pins 5 & 6	JP1/ Pins 7 & 8	JP1/ Pins 9 & 10
2	Closed	Open	Open	Open	Open
3	Open	Closed	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed
10	Open	Open	Open	Open	Open
11	Open	Open	Open	Open	Open
12	Open	Open	Open	Open	Open
15	Open	Open	Open	Open	Open

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ISI551PC

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PORT 1 ADDRESS SELECTION (CONTINUED)				
IRQ	JP1/ Pins 11 & 12	JP1/ Pins 13 & 14	JP1/ Pins 15 & 16	JP1/ Pins 17 & 18
2	Open	Open	Open	Open
3	Open	Open	Open	Open
4	Open	Open	Open	Open
5	Open	Open	Open	Open
7	Open	Open	Open	Open
10	Closed	Open	Open	Open
11	Open	Closed	Open	Open
12	Open	Open	Closed	Open
15	Open	Open	Open	Closed

SERIAL PORT ADDRESS SELECT							
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
100h	On	On	On	On	On	Off	On
108h	Off	On	On	On	On	Off	On
110h	On	Off	On	On	On	Off	On
118h	Off	Off	On	On	On	Off	On
120h	On	On	Off	On	On	Off	On
2E8h (COM 4:)	Off	On	Off	Off	Off	On	Off
2F8h (COM 2:)	Off	Off	Off	Off	Off	On	Off
3D8h	Off	Off	On	Off	Off	Off	Off
3E0h	On	On	Off	Off	Off	Off	Off
3E8h (COM 3:)	Off	On	Off	Off	Off	Off	Off
3F0h	On	Off	Off	Off	Off	Off	Off
3F8h (COM 1:)	Off	Off	Off	Off	Off	Off	Off
Note: A total of 752 memory base address settings are available. The switches are a binary representation of the decimal addresses. Switch 1 is the Least Significant Bit and switch 7 is the Most Significant Bit. The switches have the following decimal values: switch 1=8, 2=16, 3=32, 4=64, 5=128, 6=256, 7=512. Add the values of the on switches to obtain the correct memory address. (Off=1, On=0)							

MISCELLANEOUS TECHNICAL NOTES
Orientation of SW1 may vary in different revisions.