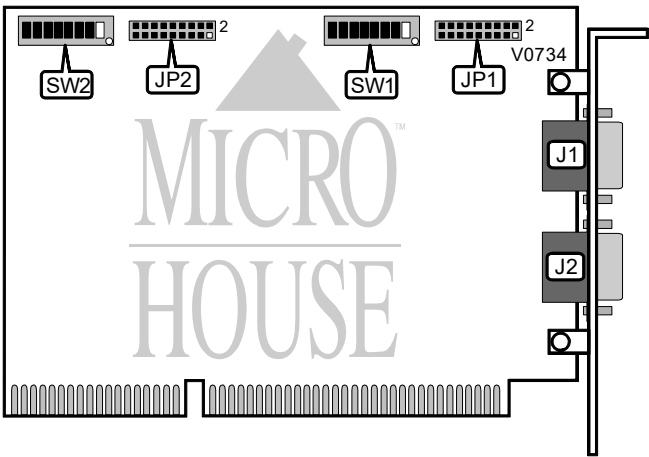


MULTI-TECH SYSTEMS, INC.
ISI552PC

Card Type
Chipset Controller
I/O Options
Maximum Dram

Serial interface
Unidentified
Serial ports (2)
N/A



CONNECTIONS	
Purpose	Location
Serial port 1	J1
Serial port 2	J2

FACTORY CONFIGURED-DO NOT ALTER	
Switch	Setting
SW1/8	Off

PORT 1 INTERRUPT SELECTION	
IRQ	JP1
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
7	Pins 9 & 10 closed
10	Pins 11 & 12 closed
11	Pins 13 & 14 closed
12	Pins 15 & 16 closed
15	Pins 17 & 18 closed

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ISI608PC

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PORT 1 INTERRUPT SELECTION	
IRQ	JP1
2	Pins 1 & 2 closed
3	Pins 3 & 4 closed
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
7	Pins 9 & 10 closed
10	Pins 11 & 12 closed
11	Pins 13 & 14 closed
12	Pins 15 & 16 closed
15	Pins 17 & 18 closed

PORT 1 ADDRESS SELECT							
Address	SW1/7	SW1/6	SW1/5	SW1/4	SW1/3	SW1/2	SW1/1
100h	Off	Off	On	Off	Off	Off	Off
108h	Off	Off	On	Off	Off	Off	On
110h	Off	Off	On	Off	Off	On	Off
118h	Off	Off	On	Off	Off	On	On
2E8h (COM4:)	On	Off	On	On	On	Off	On
2F8h (COM2:)	On	Off	On	On	On	On	On
3E0	On	On	On	On	On	Off	Off
3E8h (COM3:)	On	On	On	On	On	Off	On
3F0h	On	On	On	On	On	On	Off
3F8h (COM1:)	On	On	On	On	On	On	On

Note: A total of 752 memory base address settings are available. The switches are a binary representation of the decimal addresses. Switch 1 is the Least Significant Bit and switch 7 is the Most Significant Bit. The switches have the following decimal values: switch 1=8, 2=16, 3=32, 4=64, 5=128, 6=256, 7=512. Add the values of the on switches to obtain the correct memory address. (Off=0, On=1)

PORT 2 ADDRESS SELECT							
Address	SW2/7	SW2/6	SW2/5	SW2/4	SW2/3	SW2/2	SW2/1
100h	Off	Off	On	Off	Off	Off	Off
108h	Off	Off	On	Off	Off	Off	On
110h	Off	Off	On	Off	Off	On	Off
118h	Off	Off	On	Off	Off	On	On
2E8h (COM4:)	On	Off	On	On	On	Off	On
2F8h (COM2:)	On	Off	On	On	On	On	On
3E0	On	On	On	On	On	Off	Off
3E8h (COM3:)	On	On	On	On	On	Off	On
3F0h	On	On	On	On	On	On	Off
3F8h (COM1:)	On	On	On	On	On	On	On

Note: A total of 752 memory base address settings are available. The switches are a binary representation of the decimal addresses. Switch 1 is the Least Significant Bit and switch 7 is the Most Significant Bit. The switches have the following decimal values: switch 1=8, 2=16, 3=32, 4=64, 5=128, 6=256, 7=512. Add the values of the on switches to obtain the correct memory address. (Off=0, On=1)