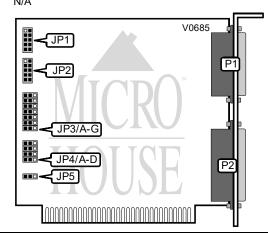
JPN CORPORATION CA8262

Card Type Chipset/Controller I/O Options Maximum DRAM

Multi-I/O Unidentified Game port, parallel port, serial ports (2)



CONNECTIONS			
Purpos :	Location	Purpos :	Location
Game port	P1	Serial port 1	JP1
Parallel port	P2	Serial port 2	JP2

SERIAL PORT 1 CONFIGURATION		
COM/Ad ress	JP3/Jump ⊧r A	JP3/Jump⊣r B
í COM1/3F8h	Pins 1 & 2 closed	Pins 1 & 2 closed
COM3/3E8h	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	N/A	Pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT SELECT - JP4				
IRQ	umper A	umper B	umper C	umper D
í IRQ4	Pins 2 & 3 open	Pins 1 & 2 closed	Pins 2 & 3 open	Pins 2 & 3 open
IRQ2	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open	Pins 1 & 2 closed
IRQ3	Pins 1 & 2 closed	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open
IRQ5	Pins 2 & 3 open	Pins 2 & 3 open	Pins 1 & 2 closed	Pins 2 & 3 open

SERIAL PORT 2 CONFIGURATION		
COM/Ad ress	JP3/Jump r C	JP3/Jump ⊧r D
í COM2/2F8h	Pins 1 & 2 closed	Pins 1 & 2 closed
COM4/2E8h	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	N/A	Pins 2 & 3 closed

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SERIAL PORT 2 INTERRUPT SELECT - JP4				
IRQ	umper A	umper B	umper C	umper D
í IRQ3	Pins 2 & 3 closed	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open
IRQ2	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open	Pins 2 & 3 closed
IRQ4	Pins 1 & 2 open	Pins 2 & 3 closed	Pins 1 & 2 open	Pins 1 & 2 open
IRQ5	Pins 1 & 2 open	Pins 1 & 2 open	Pins 2 & 3 closed	Pins 1 & 2 open

Г		PARALLEL PORT CONFIGURATION	
L			
	LPT/Adc ess	JP3/Jump∍r E	JP3/Jump ∍r F
	í LPT1/378h	Pins 1 & 2 closed	Pins 1 & 2 closed
	LPT2/278h	Pins 2 & 3 closed	Pins 1 & 2 closed
	Disabled	N/A	Pins 2 & 3 closed

PARALLEL PORT INTERRUPT SELECT		
IRQ	JP5	
í IRQ7	Pins 2 & 3 closed	
IRQ5	Pins 1 & 2 closed	

GAME	E PORT
Setting	JP3/Jumper G
í Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed