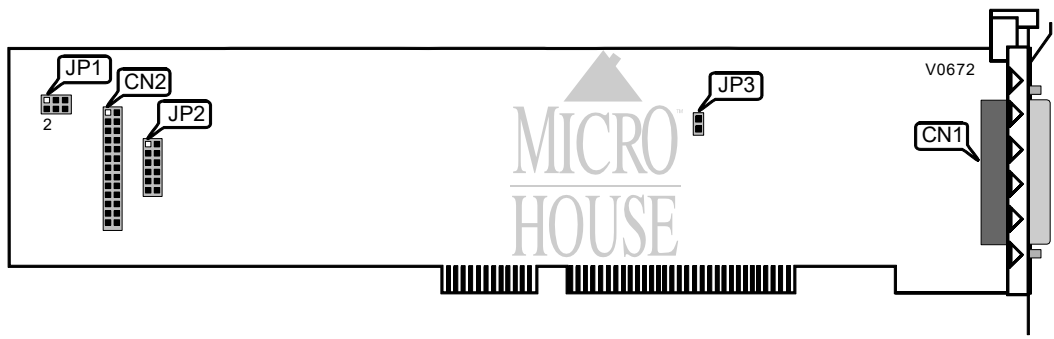# QUATECH, INC.
SP-1000/SP-1050

Card Type   Multi-I/O
Chipset Controller Unidentified
I/O Options   Parallel port (1), Serial port (1)
Maximum Dram   N/A

CONNECTIONS			
Purpose	Location	Purpose	Location
25-pin parallel port	CN1	26-pin serial port	CN2

CLOCK DIVISOR CONFIGURATION	
Divisor	JP1
1	Pins 2 & 4 closed
2	Pins 1 & 2, 5 & 6 closed
5	Pins 2 & 3, 4 & 5 closed
10	Pins 1 & 2, 4 & 5, 3 & 6 closed

FACTORY CONFIGURED SETTING	
Jumper	Setting
JP3	Do not alter

QUATECH, INC.
SP-1000/SP-1050

PARALLEL PORT CONFIGURATION
Jumpers JP2

SINGLE BOARD CONFIGURATION

JP2

TxD (2)	
(3) RxD	
RTS (4)	
(5) CTS	
DTR (20)	
(6) DSR	

TWO BOARD CONFIGURATION

JP2

TxD (2)
(3) RxD
RTS (4)
(5) CTS
DTR (20)
(6) DSR

DTE

TxD (2)
(3) RxD
RTS (4)
(5) CTS
DTR (20)
(6) DSR

DCE