AST RESEARCH, INC.
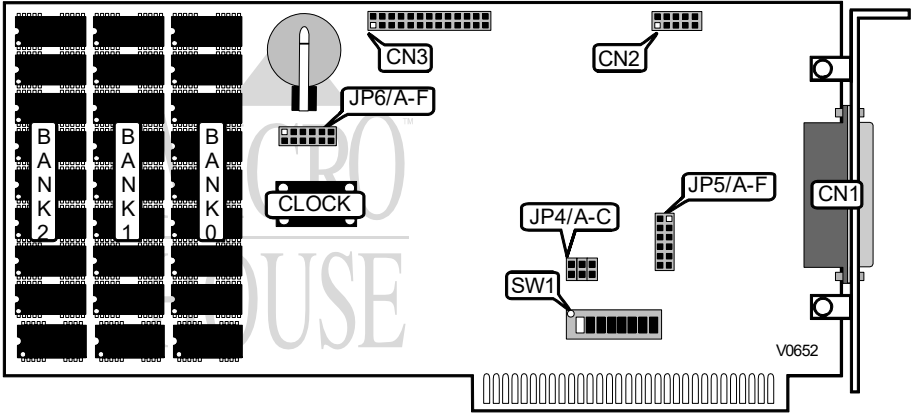# G-10 SIX PAK PLUS (MICA)

Card Type   Multi I/O
Chipset Controller Unidentified
I/O Options   (1) Parallel, (2) Serial
Maximum Dram   576KB

CONNECTIONS			
Purpose	Location	Purpose	Location
25-pin serial port external	CN1	26-pin parallel port internal	CN3
9-pin serial port Internal	CN2		

COM PORT 1 SELECT	
Setting	JP6/A
COM1 enabled	Closed
COM1 disabled	Open

COM PORT 2 SELECT	
Setting	JP6/B
COM2 enabled	Closed
COM2 disabled	Open

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G-10 SIX PAK PLUS (MICA)

LPT1 SELECT	
Setting	JP6/C
Enabled	Closed
Disabled	Open

LPT2 SELECT	
Setting	JP6/D
Enabled	Closed
Disabled	Open

GAME PORT SELECT	
Setting	JP6/E
Enabled	Closed
Disabled	Open

CLOCK SELECT	
Setting	JP6/F
Enabled	Closed
Disabled	Open

FACTORY CONFIGURED SETTING	
Swtich	Setting
SW1/7	N/A

PARITY SELECT	
Setting	SW1/8
Enabled	On
Disabled	Off

DRAM CONFIGURATION				
Starting Address	Board RAM	SW1/1	SW1/2	SW1/3
64KB	576KB	Off	Off	Off
128KB	512KB	Off	Off	On
192KB	448KB	Off	On	Off
256KB	384KB	Off	On	On
320KB	320KB	On	Off	Off
384KB	256KB	On	Off	On
448KB	192KB	On	On	Off
512KB	128KB	On	On	On

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G-10 SIX PAK PLUS (MICA)

BANK CONFIGURATION						
Bank 0	Bank 1	Bank 2	Board RAM	SW1/4	SW1/5	SW1/6
NONE	NONE	NONE	0KB	Off	Off	Off
64K	NONE	NONE	64KB	Off	Off	On
64K	64K	NONE	128KB	Off	On	Off
256K	NONE	NONE	256KB	Off	On	On
64K	256K	NONE	320KB	On	Off	Off
64K	64K	256K	384KB	On	Off	On
256K	256K	NONE	512KB	On	On	Off
64K	256K	256K	576KB	On	On	On

SERIAL PORT 1 CONFIGURATION		
Setting	JP5/A	JP5/E
Force DSR true	Closed	Open
DSR normal	Open	Closed

SERIAL PORT 1 CONFIGURATION (CONTINUED)		
Setting	JP5/C	JP5/D
Force DCD true	Closed	Open
DCD normal	Open	Closed

SERIAL PORT 1 CONFIGURATION (CONTINUED)		
Setting	JP5/E	JP5/F
Force CTS true	Closed	Open
CTS normal	Open	Closed

COM PORT INTERRUPT CONFIGURATION		
Setting	JP4/A	JP4/B
COM1 & IRQ4	Open	Closed
COM2 & IRQ3	Closed	Open

PARALLEL PORT INTERRUPT SELECT	
IRQ7	JP4/C
Enable	Closed
Disable	Open