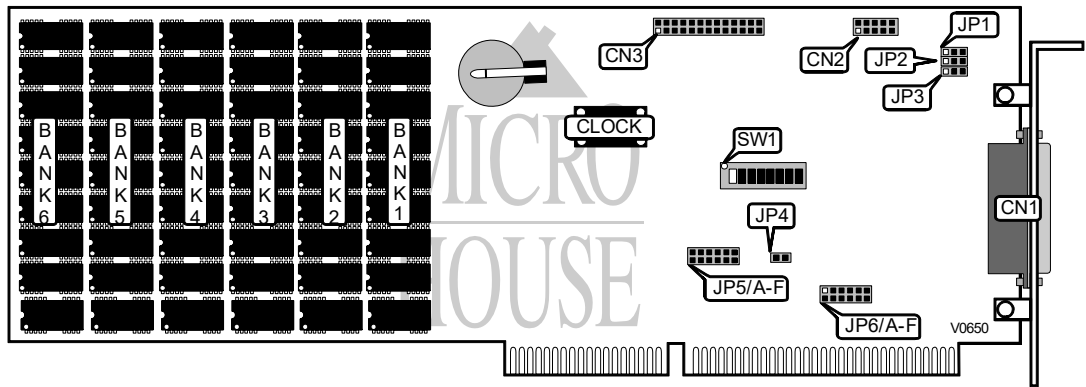# AST RESEARCH, INC.
G-8 SIX PAK PLUS

Card Type   Memory Expansion/Multi-I/O
Chipset Controller   Unidentified
I/O Options   Parallel port, Serial port (2)
Maximum Dram   384KB

CONNECTIONS			
Purpose	Location	Purpose	Location
25-pin serial port external	CN1	25-pin parallel port	CN3
9-pin serial port Internal	CN2		

PARALLEL PORT INTERRUPT SELECT	
Setting	JP4
LPT1 IRQ7 enabled	Closed
LPT1 IRQ7 disabled	Open

COM1 SELECT		
COM	JP5/A	JP5/B
COM1	Closed	Open
COM2	Open	Closed
Note: COM settings refer only to CN1.		

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PARALLEL PORT SELECT		
Setting	JP5/C	JP5/D
LPT1	Closed	Open
LPT2	Open	Closed

GAME PORT SELECT	
Setting	JP5/E
Enabled	Closed
Disabled	Open

CLOCK SELECT	
Setting	JP5/F
Enabled	Closed
Disabled	Open

FACTORY CONFIGURED SETTING	
Switch	Setting
SW1/7	N/A

PARITY SELECT	
Setting	SW1/8
Enabled	On
Disabled	Off

DRAM CONFIGURATION				
Starting Address	Board RAM	SW1/1	SW1/2	SW1/3
64KB	384KB	Off	Off	Off
128KB	384KB	Off	Off	On
192KB	384KB	Off	On	Off
256KB	384KB	Off	On	On
320KB	320KB	On	Off	Off
384KB	256KB	On	Off	On
448KB	192KB	On	On	Off
512KB	128KB	On	On	On

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BANK CONFIGURATION				
Banks Installed	Board RAM	SW1/4	SW1/5	SW1/6
None	0KB	Off	Off	Off
1	64KB	Off	Off	On
1 & 2	128KB	Off	On	Off
1 & 2 & 3	192KB	Off	On	On
1 & 2 & 3 & 4	256KB	On	Off	Off
1 & 2 & 3 & 4 & 5	320KB	On	Off	On
All Banks	384KB	On	On	Off

DRAM CONFIGURATION						
Size	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	Bank 6
64KB	(9) 64K x 1	NONE	NONE	NONE	NONE	NONE
128KB	(9) 64K x 1	(9) 64K x 1	NONE	NONE	NONE	NONE
192KB	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	NONE	NONE	NONE
256KB	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	NONE	NONE
320KB	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	NONE
384KB	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1	(9) 64K x 1

INTERRUPT CONFIGURATION				
IRQ	JP6/C	JP6/D	JP6/E	JP6/F
IRQ4	Closed	Open	Open	Open
IRQ5	Open	Closed	Open	Open
IRQ7	Open	Open	Closed	Open
IRQ2	Open	Open	Open	Closed

SERIAL PORT 1 CONFIGURATION	
Function	JP1
Force CTS true	Pins 2 & 3 closed
CTS normal	Pins 1 & 2 closed

SERIAL PORT 1 CONFIGURATION	
Function	JP2
Force DSR true	Pins 2 & 3 closed
DSR normal	Pins 1 & 2 closed

SERIAL PORT 1 CONFIGURATION	
Function	JP3
Force DCD true	Pins 2 & 3 closed
DCD normal	Pins 1 & 2 closed

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SERIAL PORT 1 IRQ4 SELECT	
IRQ4	JP6/A
Enabled	Closed
Disabled	Open

SERIAL PORT 2 IRQ3 SELECT	
IRQ3	JP6/B
Enabled	Closed
Disabled	Open