AST RESEARCH, INC.
G-11 SIX PAK 286

Card Type   Memory Expansion
Chipset Controller   Unidentified
I/O Options   Feature Connector
Maximum Dram   4MB
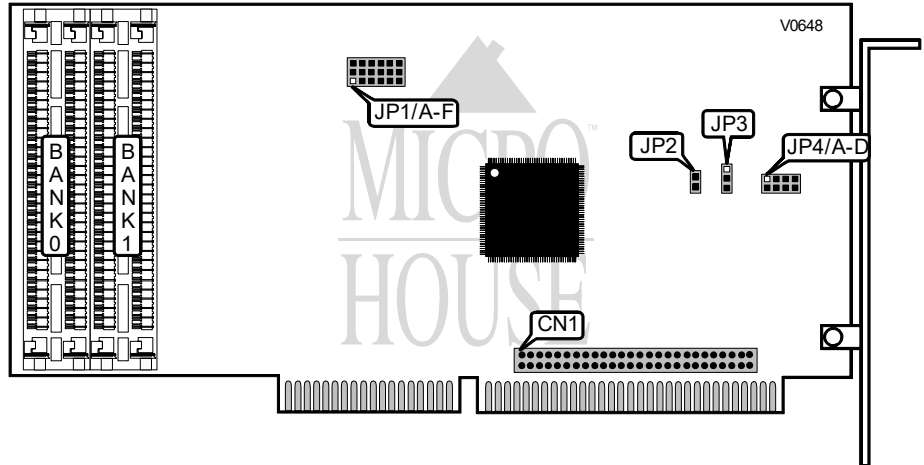
CONNECTIONS	
Purpose	**Location**
Feature Connector	CN1

PARITY SELECT	
Setting	JP2
Enable	Closed
Disable	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1024KB	(2) 256K x 9	(2) 256K x 9
2048KB	(2) 1M x 9	NONE
4096KB	(2) 1M x 9	(2) 1M x 9

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DEFAULT CONFIGURATION TOGGLE	
Purpose	JP3
Configuration default toggle first position	Pins 2 & 3 closed
Configuration default toggle second position	Pins 1 & 2 closed
Note: This toggle functions as a clearing of the firmware on the board. If the jumper is first on Pins 2 & 3 then changing this jumper to Pins 1 & 2 will cause the firmware to be erased and default values to be loaded. This will then stay until the jumper is moved again.	

DRAM SELECT						
Size	JP1/A	JP1/B	JP1/C	JP1/D	JP1/E	JP1/F
512KB	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
1024KB	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
2048KB	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
4096KB	Pins 2 &3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3
Note: Pins designated shown are in the closed position						

BOARD NUMBER				
Purpose	JP4/A	JP4/B	JP4/C	JP4/D
Board number 1	Closed	Open	Open	Open
Board number 2	Open	Closed	Open	Open
Board number 3	Open	Open	Closed	Open
Board number 4	Open	Open	Open	Closed