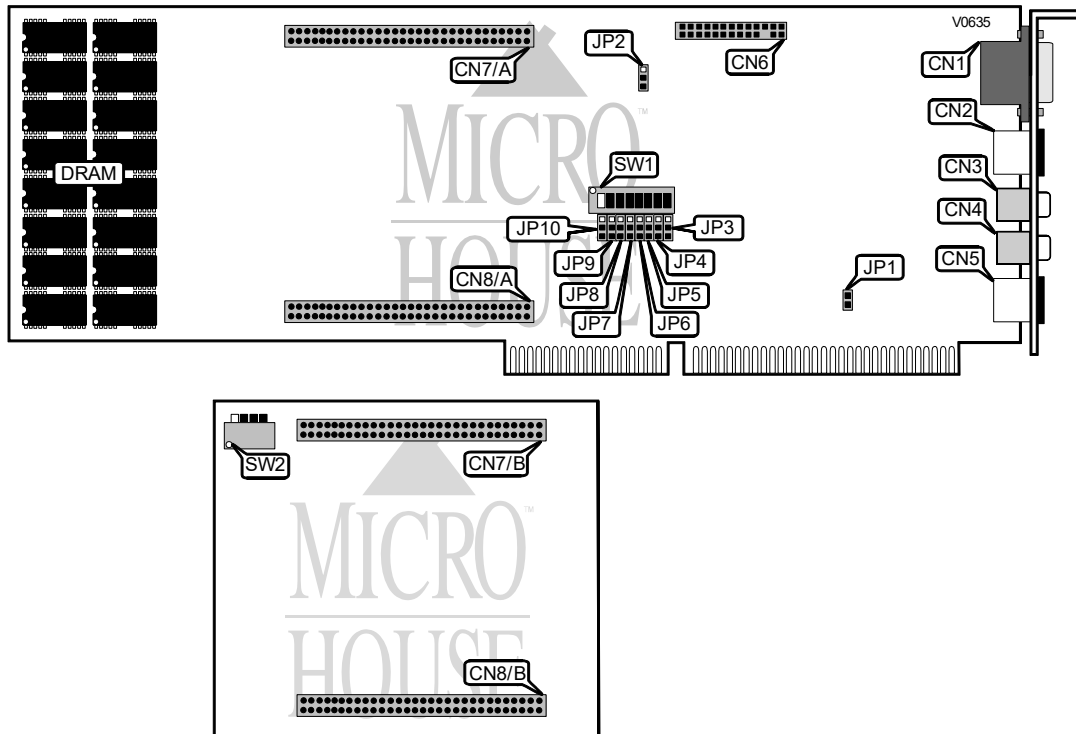


DIAMOND MULTIMEDIA SYSTEMS, INC. VIDEOSTAR

Category	Video/Video Capture
Video Types Supported	XVGA
Video Processor	Unidentified
Highest Resolution Supported	800 x 600
Data Bus Type	16-bit ISA
Memory Type	DRAM
Maximum Onboard Memory	1MB



CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video connector	CN1	VGA feature connector	CN6
S-VHS video capture input 1	CN2	60-pin MJPEG connector 1	CN7/A
RCA connector video source 0	CN3	60-pin Daughter board connector	CN7/B
RCA connector video source 1	CN4	60-pin MJPEG connector 2	CN8/A
S-VHS video capture input 2	CN5	60-pin Daughter board connector	CN8/B
Note: CN7 & CN8 are connectors for a MJPEG daughter board. This board is needed for video capture of 30 frames per second 320 x 240 standard input from a VHS source.			

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DIAMOND MULTIMEDIA SYSTEMS, INC.
VIDEOSTAR

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INTERRUPT SELECT				
IRQ	JP9	JP3	JP4	JP5
í IRQ5	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open
IRQ12	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed
IRQ14	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open
IRQ15	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open

INTERRUPT SELECT (CONTINUED)				
IRQ	JP6	JP7	JP8	JP10
IRQ3	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed
IRQ7	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open
IRQ10	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open
IRQ11	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open

ZERO WAIT STATE SELECT	
Purpose	JP1
í Enabled	Closed
Disabled	Open

FEATURE CLOCK CONFIGURATION	
Purpose	JP2
í Standard feature connector clock	Pins 1 & 2 closed
Feature connector clock delay of 5 nsec	Pins 2 & 3 closed

DAUGHTER BOARD INTERRUPT SELECT				
IRQ	JP7	JP8	JP9	JP10
IRQ3	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed
IRQ5	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open
IRQ7	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open
IRQ10	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open

DAUGHTER BOARD INTERRUPT SELECT (CONTINUED)				
IRQ	JP3	JP4	JP5	JP6
í IRQ11	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed
IRQ12	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open
IRQ14	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open
IRQ15	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open

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DAUGHTER BOARD ADDRESS SELECT				
Address	SW2/1	SW2/2	SW2/3	SW2/4
200 - 20Fh	On	On	On	On
280 - 28Fh	On	Off	On	On
300 - 30Fh	Off	On	On	On
380 - 38Fh	Off	Off	On	On

Note: The switch is located on the chip side of the daughter board, along one edge.

[illegible]