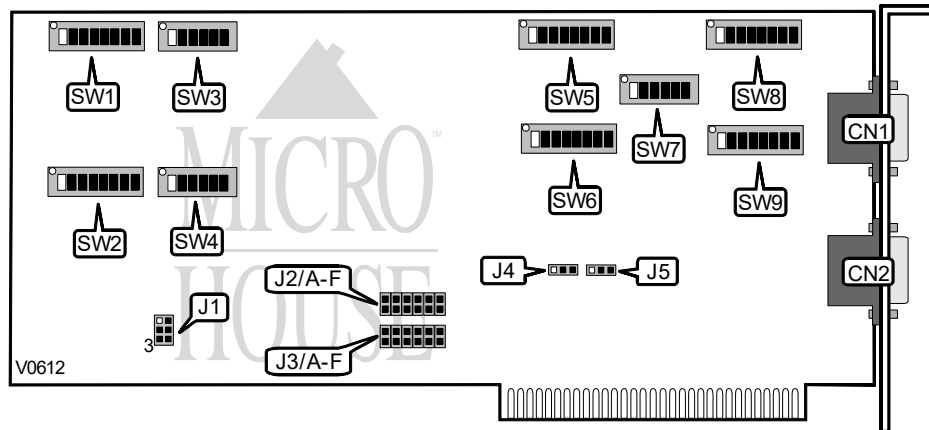


QUATECH, INC.

DCL-624

Card Type	Serial controller
Chipset/Controller	8250 UART
I/O Options	Serial ports (2)
Maximum DRAM	N/A



CONNECTIONS			
Purpose :	Location	Purpose :	Location
Serial port 1 - DB-9	CN1	Serial port 2 - DB-9	CN2

INPUT CLOCK CONFIGURATION	
MHz	J1
1.8432MHz	pins 1 & 2, 3 & 6, 4 & 5
3.6864MHz	pins 1 & 2, 4 & 5 closed
9.216MHz	pins 2 & 3, 4 & 5 closed
18.432MHz	pins 2 & 5 closed

PORT 1 INTERRUPT SELECT - J2						
IRQ	Jumper A	Jumper B	Jumper C	Jumper D	Jumper E	Jumper F
IRQ4	open	open	closed	open	open	open
IRQ2	closed	open	open	open	open	open
IRQ3	open	closed	open	open	open	open
IRQ5	open	open	open	closed	open	open
IRQ6	open	open	open	open	closed	open
IRQ7	open	open	open	open	open	closed

PORT 2 INTERRUPT SELECT - J3						
IRQ	Jumper A	Jumper B	Jumper C	Jumper D	Jumper E	Jumper F
IRQ3	open	closed	open	open	open	open
IRQ2	closed	open	open	open	open	open

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INTERRUPT SELECT - J3						
IRQ	Jumper A	Jumper B	Jumper C	Jumper D	Jumper E	Jumper F
IRQ4	open	open	closed	open	open	open
IRQ5	open	open	open	closed	open	open
IRQ6	open	open	open	open	closed	open
IRQ7	open	open	open	open	open	closed

POWER SUPPLY SELECT		
Setting	J4	J5
Computer power supply	pins 2 & 3 closed	pins 2 & 3 closed
Isolated power supply	pins 1 & 2 closed	pins 1 & 2 closed

PORT 1 I/O ADDRESS CONFIGURATION		
Base Address	SW1	SW3
3F8h	1, 2, 3, 4, 5 & 6 on	6 on
3220h	1, 2, 5, 6 & 8 on	1, 2, 4, 5 & 6 on
Note: The address range for the DCL-624 is from 0 to FFFFh. The switches are a binary representation of the addresses. When a switch is off, the corresponding bit is set to 1 and has the following decimal value: SW1/1=8, SW1/2=4, SW1/3=2, SW1/4=1, SW1/5=8, SW1/6=4, SW1/7=2, SW1/8=1, SW3/1=8, SW3/2=4, SW3/3=2, SW3/4=1, SW3/5=8. Port 1 requires 8 consecutive address locations.		

PORT 2 I/O ADDRESS CONFIGURATION		
Base Address	SW2	SW4
2F8h	1, 3, 4, 5, 6 & 8 on	6 on
3220h	1, 2, 5, 6 & 8 on	1, 2, 4, 5 & 6 on
Note: The address range for the DCL-624 is from 0 to FFFFh. The switches are a binary representation of the addresses. When a switch is off, the corresponding bit is set to 1 and has the following decimal value: SW2/1=8, SW2/2=4, SW2/3=2, SW2/4=1, SW2/5=8, SW2/6=4, SW2/7=2, SW2/8=1, SW4/1=8, SW4/2=4, SW4/3=2, SW4/4=1, SW4/5=8. Port 2 requires 8 consecutive address locations.		

PORT 1 ENABLE/DISABLE	
Setting	SW3/6
Enabled	on
Disabled	off

PORT 2 ENABLE/DISABLE	
Setting	SW4/6
Enabled	on
Disabled	off

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PORT 1 TRANSMISSION OPTIONS - SW5								
Setting	SW5/1	SW5/2	SW5/3	SW5/4	SW5/5	SW5/6	SW5/7	SW5/8
120mA Active Transmit	off	off	on	on	on	off	off	off
30mA Active Transmit	off	off	on	on	on	on	off	off
60mA Active Transmit	off	off	on	on	on	on	on	off
Passive Transmit	on	on	off	off	off	off	off	off

PORT 2 TRANSMISSION OPTIONS - SW6								
Setting	SW6/1	SW6/2	SW6/3	SW6/4	SW6/5	SW6/6	SW6/7	SW6/8
120mA Active Transmit	off	off	on	on	on	off	off	off
30mA Active Transmit	off	off	on	on	on	on	off	off
60mA Active Transmit	off	off	on	on	on	on	on	off
Passive Transmit	on	on	off	off	off	off	off	off

PORT 1 RECEIVE CIRCUIT CURRENT OPTIONS - SW7			
Setting	SW7/1	SW7/2	SW7/3
120mA	on	off	off
30mA	off	on	off
60mA	off	off	on

PORT 2 RECEIVE CIRCUIT CURRENT OPTIONS - SW7			
Setting	SW7/4	SW7/5	SW7/6
120mA	on	off	off
30mA	off	on	off
60mA	off	off	on

PORT 1 RECEIVE OPTIONS - SW8								
Setting	SW8/1	SW8/2	SW8/3	SW8/4	SW8/5	SW8/6	SW8/7	SW8/8
Passive Receive	on	on	off	off	off	off	off	off
20mA Active Receive	off	off	on	on	on	off	off	off
30mA Active Receive	off	off	on	on	on	on	off	off
60mA Active Receive	off	off	on	on	on	on	on	off

PORT 2 RECEIVE OPTIONS - SW9								
Setting	SW9/1	SW9/2	SW9/3	SW9/4	SW9/5	SW9/6	SW9/7	SW9/8
Passive Receive	on	on	off	off	off	off	off	off
20mA Active Receive	off	off	on	on	on	off	off	off
30mA Active Receive	off	off	on	on	on	on	off	off
60mA Active Receive	off	off	on	on	on	on	on	off