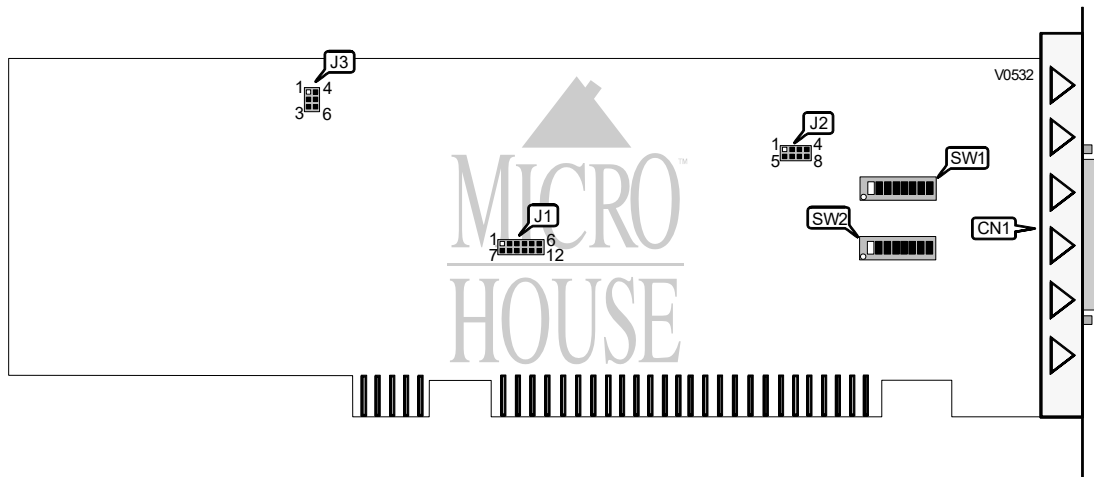


QUATECH, INC.
SCL-6100

Card Type	Serial controller
Chipset/Controller	Unidentified UART
I/O Options	Serial I/O port
Maximum DRAM	N/A



CONNECTIONS	
Purpose	Location
DB-25 serial I/O port	CN1

CLOCK FREQUENCY	
Frequency	J3
1.8432MHz	Pins 1 & 2, 3 & 6, 4 & 5 closed
3.6864MHz	Pins 2 & 3, 4 & 5 closed
9.216MHz	Pins 1 & 2, 5 & 6 closed
18.432MHz	Pins 2 & 5 closed

OUTPUT CONFIGURATION								
Setting	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
Active transmit 20mA	Off	Off	On	On	On	Off	Off	Off
Passive transmit	On	On	Off	Off	Off	Off	Off	Off
Active transmit 30mA	Off	Off	On	On	On	On	Off	Off
Active transmit 60mA	Off	Off	On	On	On	On	On	Off
RS-232 transmit	Off	Off	Off	Off	Off	Off	Off	On


Continued on next page . . .

QUATECH, INC.
SCL-6100

... continued from previous page

INPUT CONFIGURATION								
Setting	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
Passive receive	On	On	Off	Off	Off	Off	Off	Off
Active receive 20mA	Off	Off	On	On	On	Off	Off	Off
Active receive 30mA	Off	Off	On	On	On	On	Off	Off
Active receive 60mA	Off	Off	On	On	On	On	On	Off
RS-232 receive	Off	Off	Off	Off	Off	Off	Off	On

CURRENT THRESHOLD SELECT	
Setting	J2
20mA	Pins 1 & 5, 4 & 8 closed
30mA	Pins 2 & 6, 4 & 8 closed
60mA	Pins 3 & 4, 4 & 8 closed
RS-232 receive	Pins 2 & 3, 6 & 7 closed

DTE/DCE CONFIGURATION	
J1	
DTE(Default) 	DCE 