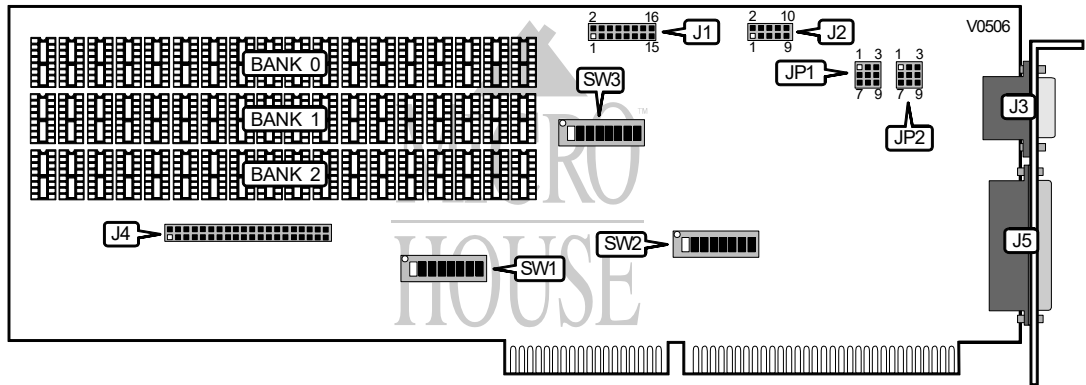# UNIDENTIFIED
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Card Type   Multi I/O card
Chipset/Controller   Unidentified
I/O Options   Memory, game port, parallel port, serial port (2).
Maximum DRAM   1.5MB

CONNECTIONS			
Purpose	Location	Purpose	Location
Game port	J1	Parallel port	J5
Serial port 2 - internal	J2	MF-3001 connector	J4
Serial port 1 - external	J3		

FACTORY CONFIGURED SETTINGS	
Switch	Setting
SW3/7	N/A
SW3/8	N/A

GAME PORT CONFIGURATION	
Setting	SW2/4
í Enabled	On
Disabled	Off

PARALLEL PORT CONFIGURATION				
LPT/Address/IRQ	SW2/7	SW2/8	SW3/5	SW3/6
í LPT1/378h/IRQ7	Off	On	Off	On
LPT1/378h/IRQ5	Off	On	On	Off
LPT2/278h/IRQ7	On	Off	Off	On
LPT2/278h/IRQ5	On	Off	On	Off
Disabled	Off	Off	Off	Off

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SERIAL PORT 1 & 2 CONFIGURATION			
Port 1	Port 2	SW2/5	SW2/6
COM1/3F8h	COM2/2F8h	Off	On
COM2/2F8h	COM3/2E8h	On	Off
COM3/2E8h	COM4/2E0h	On	On
Disabled	Disabled	Off	Off

SERIAL PORT 1 INTERRUPT SELECT		
Port 1	SW3/3	SW3/4
IRQ4	Off	On
IRQ3	On	Off
Disabled	Off	Off

SERIAL PORT 2 INTERRUPT SELECT		
Port 2	SW3/1	SW3/2
IRQ3	On	Off
IRQ4	Off	On
Disabled	Off	Off

SERIAL PORT 1 INTERFACE CONFIGURATION	
Setting	JP2
Normal	pins 1 & 2, 4 & 5, 7 & 8 closed
Forced true	pins 2 & 3, 5 & 6, 7 & 8 closed

SERIAL PORT 2 INTERFACE CONFIGURATION	
Setting	JP1
Normal	pins 1 & 2, 4 & 5, 7 & 8 closed
Forced true	pins 2 & 3, 5 & 6, 7 & 8 closed

STARTING MEMORY ADDRESS							
Memory/Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7
256KB/040000h	Off	On	Off	Off	Off	Off	Off
512KB/080000h	Off	Off	On	Off	Off	Off	Off
1MB/100000h	Off	Off	Off	On	Off	Off	Off
1.125MB/120000h	On	Off	Off	On	Off	Off	Off

MEMORY CONFIGURATION (64K DRAM CHIPS)								
Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	SW2/1	SW2/2	SW2/3
128KB	N/A	N/A	N/A	N/A	N/A	Off	Off	Off
128KB	128KB	N/A	N/A	N/A	N/A	On	Off	Off
128KB	128KB	128KB	N/A	N/A	N/A	Off	On	Off
128KB	128KB	128KB	128KB	128KB	128KB	On	On	Off
Note: Banks 3, 4 & 5 are located on supplemental card MF-3001								

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MEMORY CONFIGURATION (256K DRAM CHIPS)								
Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	SW2/1	SW2/2	SW2/3
512KB	N/A	N/A	N/A	N/A	N/A	Off	Off	On
512KB	512KB	N/A	N/A	N/A	N/A	On	Off	On
512KB	512KB	512KB	N/A	N/A	N/A	Off	On	On
512KB	512KB	512KB	512KB	512KB	512KB	On	On	On
Note: Banks 3, 4 & 5 are located on supplemental card MF-3001								

MEMORY PARITY CHECK	
Setting	SW1/8
í Enabled	On
Disabled	Off