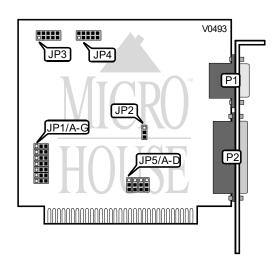
RELIALOGIC CORPORATION PRIVATE, LTD. CA8201(1S)/CA8202(2S)

Card Type Chipset/Controller I/O Options Maximum DRAM Multi I/O Unidentified

Game port, parallel port, serial ports (2)

N/A



CONNECTIONS				
Pι pose	Lc ation	Pt pose	Lc ઃation	
Serial port	JP3	Game port	P1	
Serial port	JP4	Parallel port	P2	
Note: Manufacturer documentation does not specify which serial port is primary.				

SERIAL PORT CONFIGURATION					
Port 1	Port 2	JP1/A	JP1/B	JP1/C	JP1/D
í COM1	COM2	Pins 2 & 3 closed			
COM1	COM4	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
COM3	COM2	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
COM3	COM4	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
Disabled	COM2	N/A	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
Disabled	COM4	N/A	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
COM1	Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed	N/A	Pins 1 & 2 closed
COM3	Disabled	Pins 1 & 2 closed	Pins 2 & 3 closed	N/A	Pins 1 & 2 closed

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PARALLEL PORT CONFIGURATION				
LP1	JP1/E	JP1/F		
í 1	Pins 2 & 3 closed	Pins 2 & 3 closed		
2	Pins 1 & 2 closed	Pins 2 & 3 closed		
Disabled	N/A	Pins 1 & 2 closed		

PARALLEL PORT INTERRUPT SELECT				
IRQ	JP2			
í IRQ7	Pins 2 & 3 closed			
IRQ5	Pins 1 & 2 closed			

PRIMARY SERIAL PORT INTERRUPT SELECT				
IRQ	JP5/A	JP5/B	JP5/C	JP5/D
í IRQ4	Open	Pins 1 & 2 closed	Open	Open
IRQ3	Pins 1 & 2 closed	Open	Open	Open
IRQ5	Open	Open	Pins 1 & 2 closed	Open
IRQ2	Open	Open	Open	Pins 1 & 2 closed

ALTERNATE SERIAL PORT INTERRUPT SELECT				
IRQ	JP5/A	JP5/B	JP5/C	JP5/D
í IRQ3	Pins 2 & 3 closed	Open	Open	Open
IRQ4	Open	Pins 2 & 3 closed	Open	Open
IRQ5	Open	Open	Pins 2 & 3 closed	Open
IRQ2	Open	Open	Open	Pins 2 & 3 closed

GAME PORT CONFIGURATION				
Setting	JP1/G			
í Enabled	Pins 2 & 3 closed			
Disabled	Pins 1 & 2 closed			