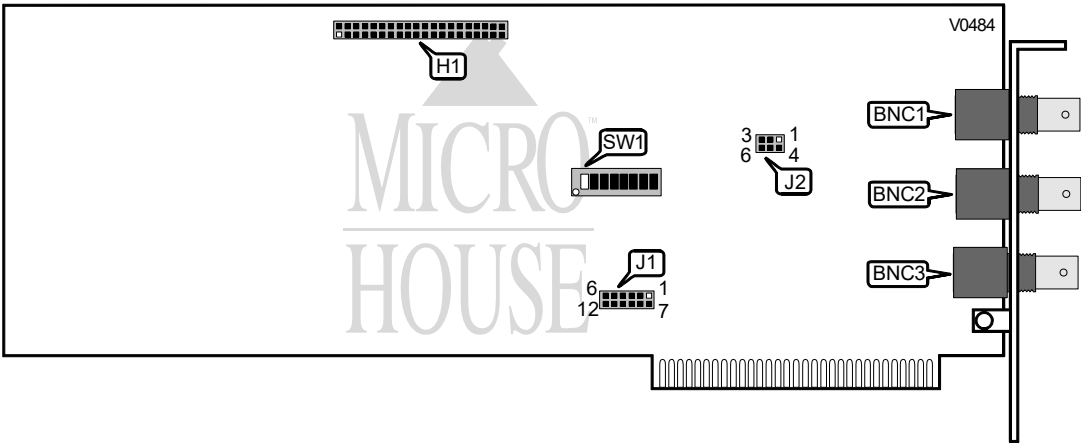


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Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Waveform Synthesizer
Unidentified
Analog/Clock/Digital/Strobe I/O ports
N/A



CONNECTIONS			
Position	Location	Position	Location
Analog output	BNC1	Clock/Trigger input	BNC3
Strobe/Clock output	BNC2	Digital output port	H1

ADDRESS SELECT								
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
2E0h	On	Off	Off	Off	On	N/A	N/A	On
330h	Off	On	On	Off	Off	N/A	N/A	On
350h	Off	On	Off	On	Off	N/A	N/A	Off

INTERRUPT SELECT	
IRQ	J1
IRQ0	Pins 1 & 2 closed
IRQ1	Pins 6 & 12 closed
IRQ2	Pins 5 & 11 closed
IRQ3	Pins 4 & 10 closed
IRQ4	Pins 3 & 9 closed
IRQ5	Pins 2 & 8 closed
IRQ6	Pins 1 & 7 closed
IRQ7	

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I/O CONFIGURATION		
Port	Function	J1
1 BNC2	Strobe output	Pins 2 & 3 closed
BNC2	Clock output	Pins 3 & 4 closed
1 BNC3	External clock input	Pins 7 & 8 closed
BNC3	External trigger input	Pins 6 & 7 closed