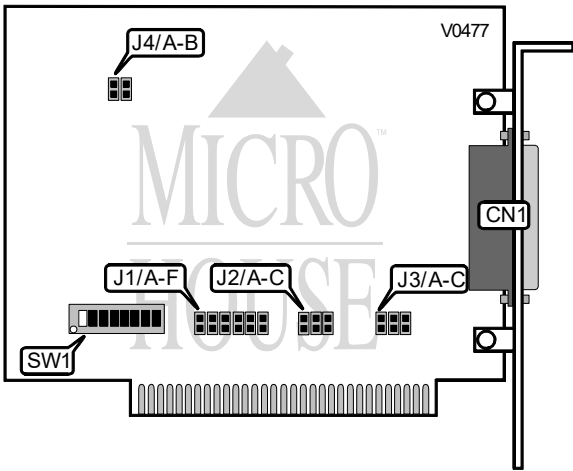


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Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Serial I/O controller
Unidentified
I/O port
N/A



CONNECTIONS	
Purpose	Location
I/O interface	J1

INTERRUPT SOURCE		
Source	J4/A	J4/B
í Terminal count on DMA transfer	Closed	Open
TMS9914A	Open	Closed

INTERRUPT SELECT						
IRQ	J1/A	J1/B	J1/C	J1/D	J1/E	J1/F
IRQ2	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed

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DMA CONFIGURATION						
DMA	J2/A	J2/B	J2/C	J3/A	J3/B	J3/C
1	Closed	Open	Open	Closed	Open	Open
2	Open	Closed	Open	Open	Closed	Open
3	Open	Open	Closed	Open	Open	Closed

ADDRESS SELECT								
Address	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
i 318h	Off	Off	On	On	On	Off	Off	Off
210h	Off	On	On	On	On	Off	On	Off
360h	Off	Off	On	Off	Off	On	On	Off