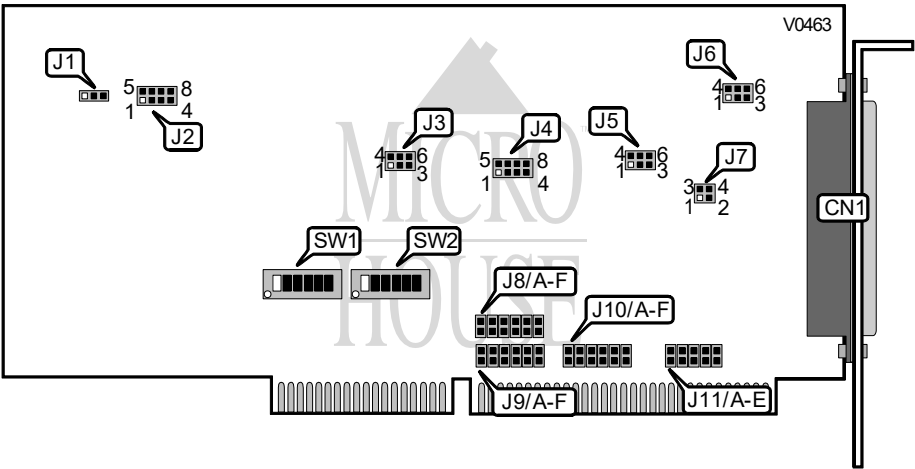


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DAQ-16

Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Data acquisition adapter
Analog Devices
Digital/Analog converter
N/A



CONNECTIONS	
Purpose	Location
DB-62 converter interface	CN1

INPUT GAIN SELECT	
Gain	J7
1	Pins 3 & 4 closed
10	Pins 2 & 4 closed
100	Pins 1 & 3 closed

INPUT RANGE SELECT	
Range	J6
10 volt	Pins 2 & 3, 5 & 6 closed
5 volt	Pins 1 & 2, 4 & 5 closed
2.5 volt	Pins 2 & 3, 4 & 5 closed

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OPERATING MODE	
Mode	J5
Uni-polar, binary	Pins 1 & 2, 4 & 5 closed
Uni-polar, 2's complement	Pins 1 & 2, 5 & 6 closed
Bi-polar, binary	Pins 2 & 3, 4 & 5 closed
Bi-polar, 2's complement	Pins 2 & 3, 5 & 6 closed

D/A CHANNEL POWER REFERENCE SOURCE		
Channel	Source	J3
0	Internal	Pins 4 & 5 closed
0	External	Pins 5 & 6 closed
1	Internal	Pins 1 & 2 closed
1	External	Pins 2 & 3 closed

CHANNEL 0 OUTPUT CONFIGURATION		
Mode	Gain	J4
Bi-polar	1	Pins 1 & 5, 2 & 6 closed
Bi-polar	2	Pins 1 & 5, 2 & 6 closed
Uni-polar	1	Pins 1 & 5, 2 & 6 closed
Uni-polar	2	Pins 1 & 5, 2 & 6 closed

CHANNEL 1 OUTPUT CONFIGURATION		
Mode	Gain	J4
Bi-polar	1	Pins 3 & 7, 4 & 8 closed
Uni-polar	2	Pins 3 & 7, 4 & 8 closed
Uni-polar	1	Pins 3 & 7, 4 & 8 closed
Uni-polar	2	Pins 3 & 7, 4 & 8 closed

INTERRUPT SELECT						
IRQ	J10/A	J10/B	J10/C	J10/D	J10/E	J10/F
IRQ2/9	Closed	Open	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open	Open
IRQ5	Open	Open	Open	Closed	Open	Open
IRQ6	Open	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Open	Closed

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INTERRUPT SELECT					
IRQ	J11/A	J11/B	J11/C	J11/D	J11/E
IRQ10	Closed	Open	Open	Open	Open
IRQ11	Open	Closed	Open	Open	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Open	Open	Closed	Open
IRQ15	Open	Open	Open	Open	Closed

DMA CONFIGURATION			
Channel	Level	J8	J9
0	7	Pins 1 & 7, 2 & 8 closed	N/A
0	6	Pins 3 & 9, 4 & 10 closed	N/A
0	5	Pins 5 & 11, 6 & 12 closed	N/A
1	7	N/A	Pins 1 & 7, 2 & 8 closed
1	6	N/A	Pins 3 & 9, 4 & 10 closed
1	5	N/A	Pins 5 & 11, 6 & 12 closed

A/D CONVERSION START PULSE	
Setting	J1
í DAQ-16 controlled	Pins 1 & 2 closed
User controlled	Pins 2 & 3 closed

TIMER SAMPLING CONFIGURATION	
Setting	J2
í Internal timer, 2 cascaded	Pins 1 & 2, 6 & 7 closed
Internal timer, 3 cascaded	Pins 2 & 6, 7 & 8 closed
External timer, without pre-divider	Pins 1 & 2, 3 & 7 closed
External timer, with pre-divider	Pins 1 & 5, 3 & 7 closed