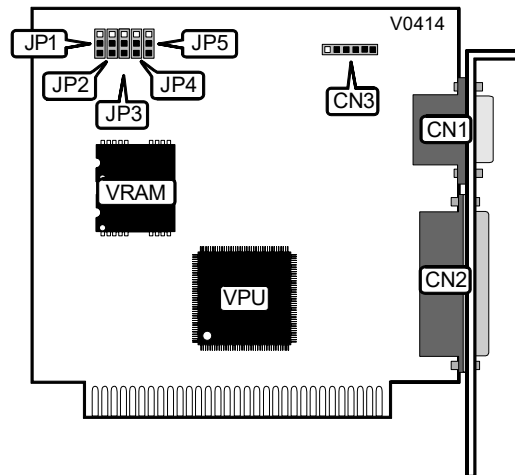


PINE TECHNOLOGY

PT-501

Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Video
 SARC
 Light pen (optional), parallel port, video port
 64KB



CONNECTIONS			
Purpose :	Location	Purpose :	Location
15-pin analog video port	CN1	Light pen connector (optional)	JP6
Parallel port	CN2		

MONITOR TYPE	
Type	JP1
Monochrome	Pins 1 & 2 closed
Color	Pins 2 & 3 closed

PARALLEL PORT CONFIGURATION		
Address	JP2	JP3
3BCh	Pins 1 & 2 closed	Pins 1 & 2 closed
378h	Pins 1 & 2 closed	Pins 2 & 3 closed
278h	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

INTERNAL/EXTERNAL CGA	
Setting	JP4
Internal	Pins 1 & 2 closed
External	Pins 2 & 3 closed

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PINE TECHNOLOGY
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EMULATION MODE	
Setting	JP5
Disabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed