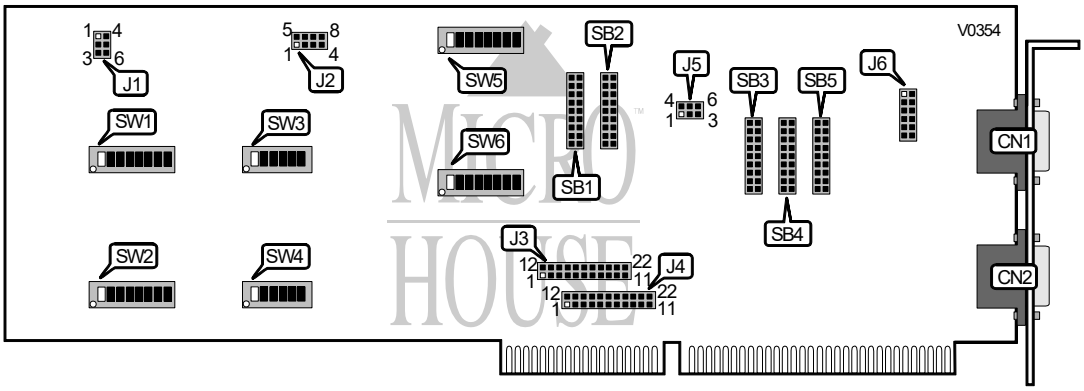
QUATECH, INC.
RW-100T

Card Type I/O controller
Chipset/Controller Unidentified UART
I/O Options Serial ports (2)
Maximum DRAM N/A



CONNECTIONS			
Purpos ›	Location	Purpos ›	Location
Serial port 1	CN1	Serial port 2	CN2

CLOCK SPEED SELECT	
Speed	J1
í 18.432	Pins 2 & 5 closed
9.216	Pins 1 & 2, 5 & 6 closed
3.6864	Pins 2 & 3, 4 & 5 closed
1.8432	Pins 1 & 2, 3 & 6, 4 & 5 closed

PRIMARY PORT I/O ADDRESS SELECT		
Address	SW1	SW3
í 03F8h	1 & 2, 3 & 4, 5 & 6 on	6 on
03E8h	1 & 2, 3 & 4, 5 & 6 on	4 & 6 on
3220h	1 & 2, 5 & 6, 8 on	1 & 2, 4 & 5, 6 on
Note: The address range for the RW-100T is from 0 to FFFFh. The switches are a binary representation of the addresses. The switches have the following decimal values: SW1/1=8, SW1/2=4, SW1/3=2, SW1/4=1, SW1/5=8, SW1/6=4, SW1/7=2, SW1/8=1, SW3/1=8, SW3/2=4, SW3/3=2, SW3/4=1, SW3/5=8.		

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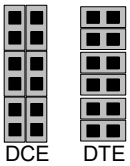
SECONDARY PORT I/O ADDRESS SELECT		
Address	SW2	SW4
í 02F8h	1 & 2, 3 & 4, 5 &6, 8 on	6 on
02E8h	1 & 2, 3 & 4, 5 & 6, 8 on	4 & 6 on
5228h	1 & 3, 5 & 6, 8 on	1 & 2, 4 & 6 on
Note: The address range for the RW-100T is from 0 to FFFFh. The switches are a binary representations of the addresses. The switches have the following decimal values: SW2/1=8, SW2/2=4, SW2/3=2, SW2/4=1, SW2/5=8, SW2/6=4, SW2/7=2, SW2/8=1, SW4/1=8, SW4/2=4, SW4/3=2, SW4/4=1, SW4/5=8.		

INTERRUPT SELECT		
IRQ	J3 (Port)	J4 (Port ?)
IRQ2	Pins 1 & 12 closed	Pins 1 & 12 closed
IRQ3	Pins 2 & 13 closed	Pins 2 & 13 closed
IRQ4	Pins 3 & 14 closed	Pins 3 & 14 closed
IRQ5	Pins 4 & 15 closed	Pins 4 & 15 closed
IRQ6	Pins 5 & 16 closed	Pins 5 & 16 closed
IRQ7	Pins 6 & 17 closed	Pins 6 & 17 closed
IRQ10	Pins 7 & 18 closed	Pins 7 & 18 closed
IRQ11	Pins 8 & 19 closed	Pins 8 & 19 closed
IRQ12	Pins 9 & 20 closed	Pins 9 & 20 closed
IRQ14	Pins 10 & 21 closed	Pins 10 & 21 closed
IRQ15	Pins 11 & 22 closed	Pins 11 & 22 closed

SECONDARY SERIAL PORT OUTPUT CONFIGURATION	
Output	Protocol Selectors Installe
RS-232C DTE	SB1 & SB4
RS-232C DCE	SB1 & SB5
RS-485	SB2 & SB3
Note: These settings are valid only when no protocol selectors are installed on any jumpers other than those indicated. Incorrect combinations of these selectors may result in card or system damage.	

PRIMARY SERIAL PORT DTE/DCE CONFIGURATION
Serial Port CN1
Jumper J6

DCE DTE

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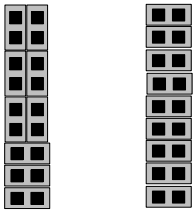
SECONDARY SERIAL PORT DTE/DCE CONFIGURATION
Serial Port CN2
Jumpers SB1 - SB5



DCE DTE

AUXILIARY I/O CONFIGURATION	
Function	J5
RTS/CTS loopback	Pins 3 & 6 closed
Transmit RTS	Pins 2 & 3 closed
Receive CTS	Pins 5 & 6 closed
RCLK/XCLK loopback	Pins 1 & 4 closed
Transmit XCLK	Pins 1 & 2 closed
Receive RCLK	Pins 4 & 5 closed
AUX out/AUX in loopback	Pins 2 & 5 closed
Note: The default setting for J5 is to have Pins 1 & 4, 2 & 5, and 3 & 6 closed.	

HARDWARE OPTION SWITCH CONFIGURATION	
Setting	J2
SW5 enabled for hardware option select for Port 1	Pins 1 & 5 closed
SW6 enabled for hardware option select for Port 2	Pins 2 & 5 closed

TRI-STATE OUTPUT CONFIGURATION	
Setting	J2
Tri-state enabled for Port 1	Pins 3 & 7 closed
Tri-state enabled for Port 2	Pins 4 & 8 closed

USER DEFINABLE HARDWARE OPTION CONFIGURATION		
Addres	SW5	SW6
6Ah	2 & 3, 5 &7 on	2 & 3, 5 &7 on
B4h	1 & 3, 4 & 6 on	1 & 3, 4 & 6 on
Note: These switches are a binary representation of the addresses. The switches have the following decimal values: switch 1=8, switch 2=4, switch 3=2, switch 4=1, switch 5=8, switch 6=4, switch 7=2, switch 8=1.		